

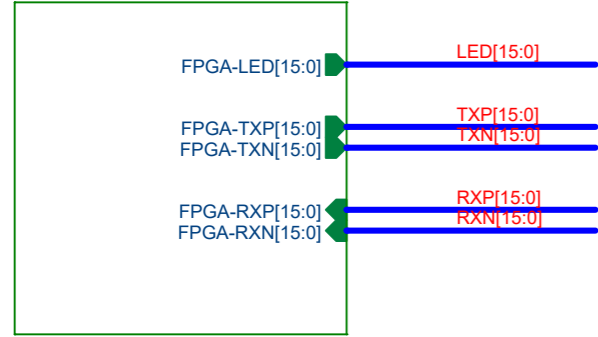
AMANEQ mezzanine card  
Clock Data Distributor Optical  
(CDD-OPT)

Rev.3  
2022.03.02  
R. Honda

(GN-2140-2)

Title		
TITLE		
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BASECN



[3000]BASECN

TRANSLATE

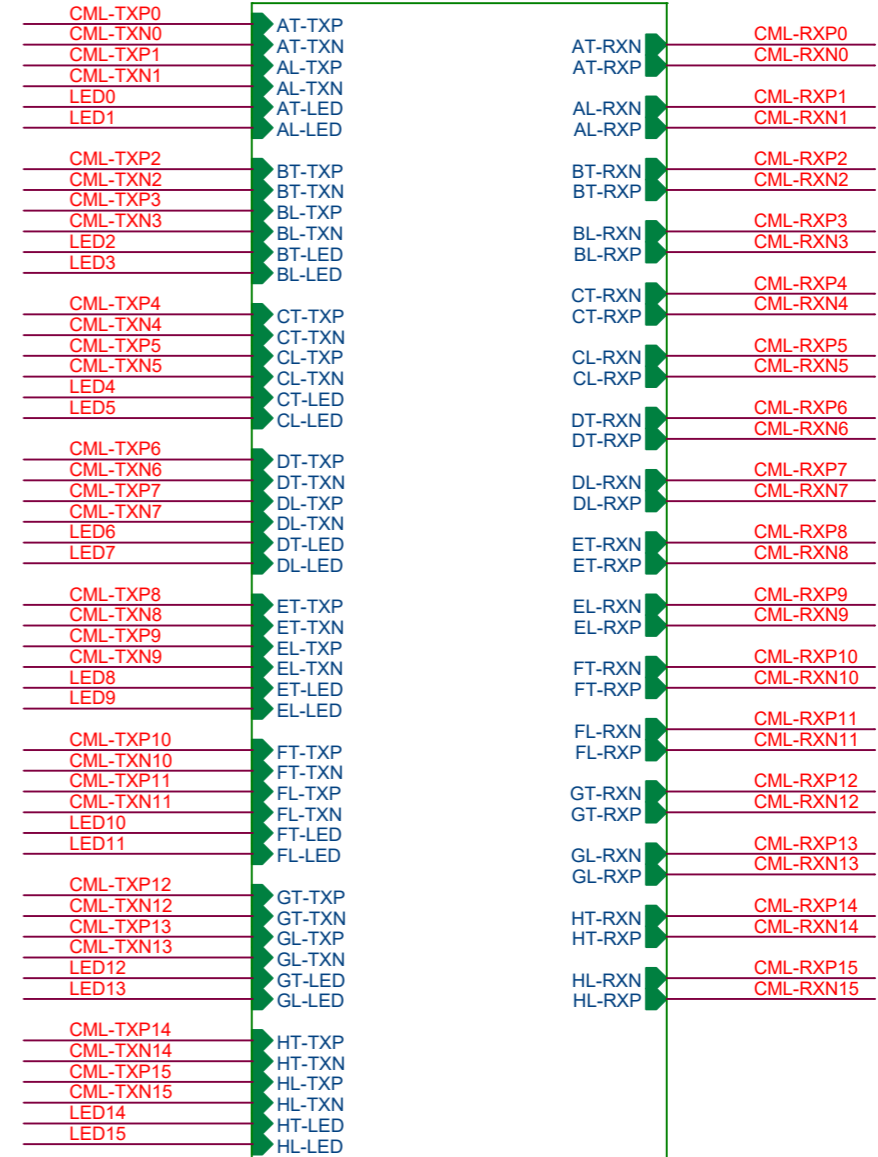


[2000]TRANSLATOR

TXとRXではTXの配線を優先してください。  
TXは部品面を通してRXと交差する場合、  
RXを半田面か内層へ逃がしてください。

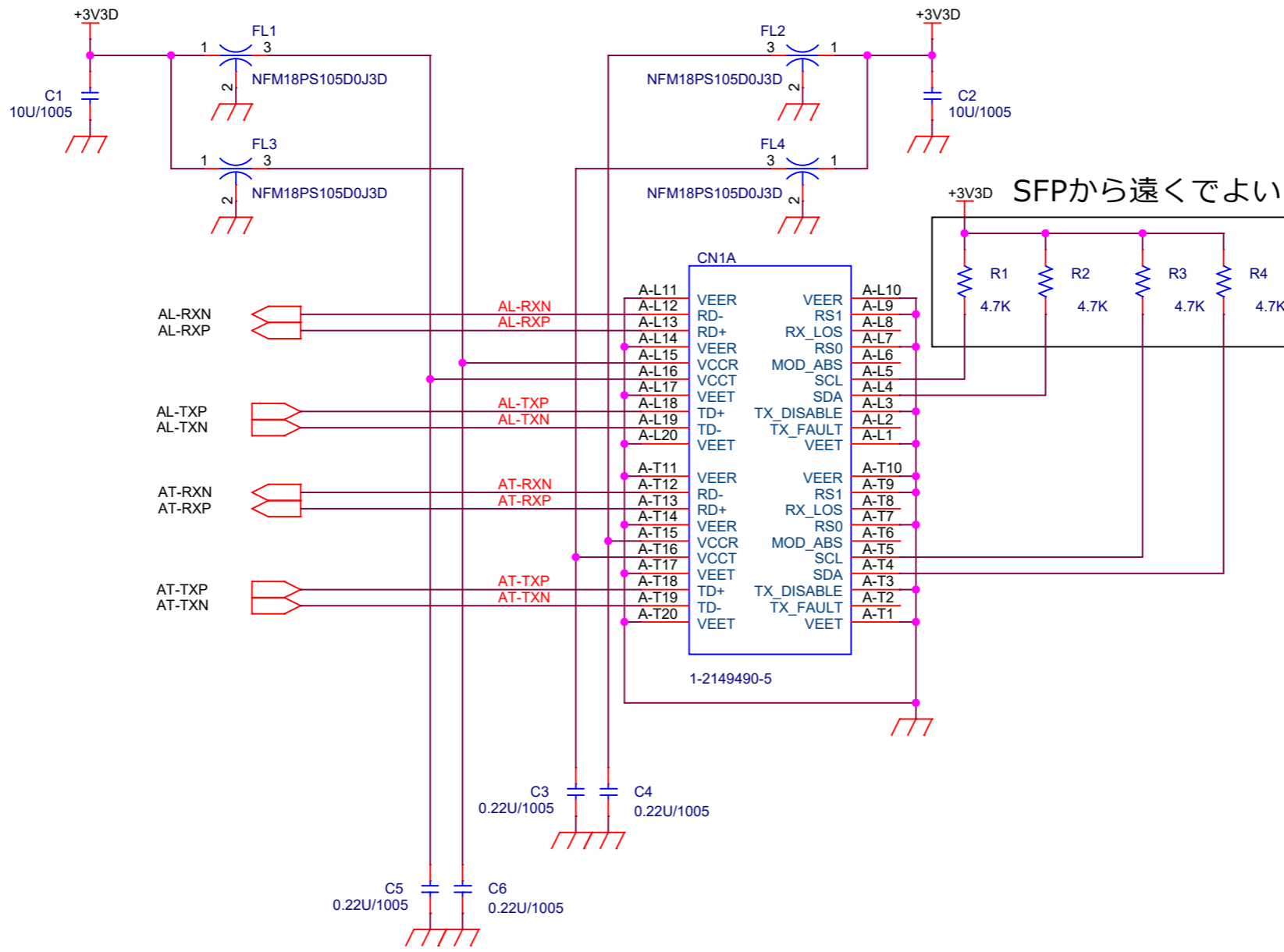
TXとRXは差動100Ωです。

SFP-2x6

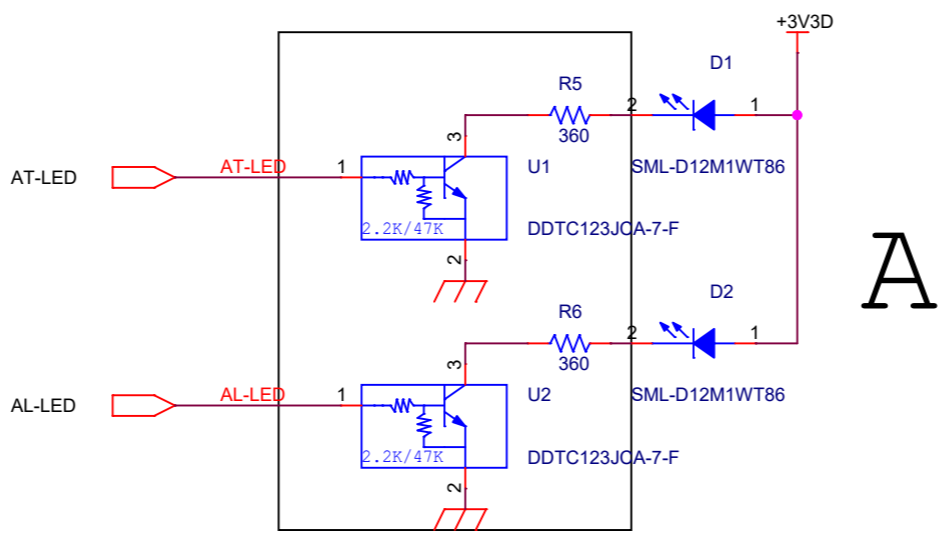


[1000]SFP

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TOPLEVEL		
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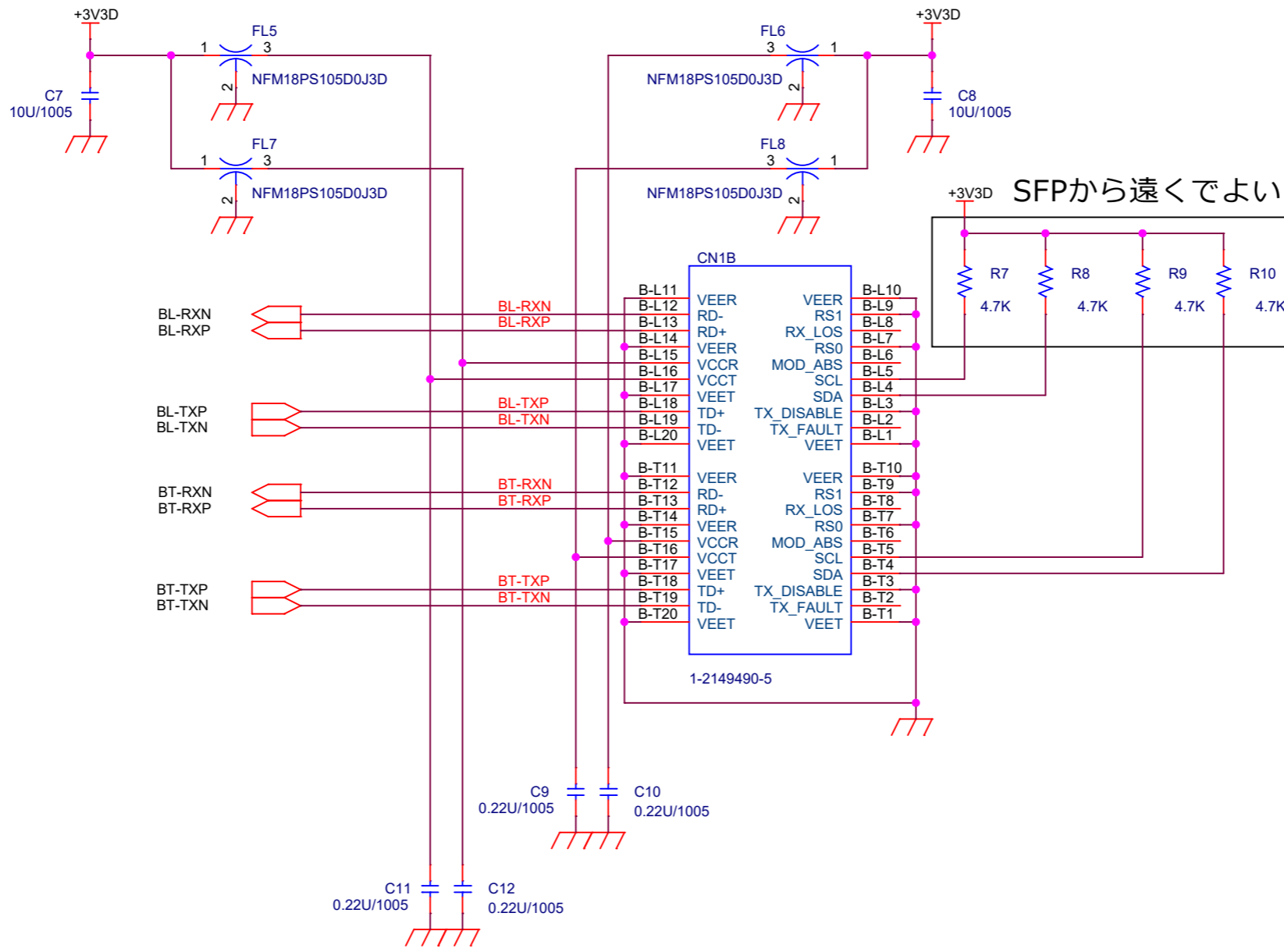
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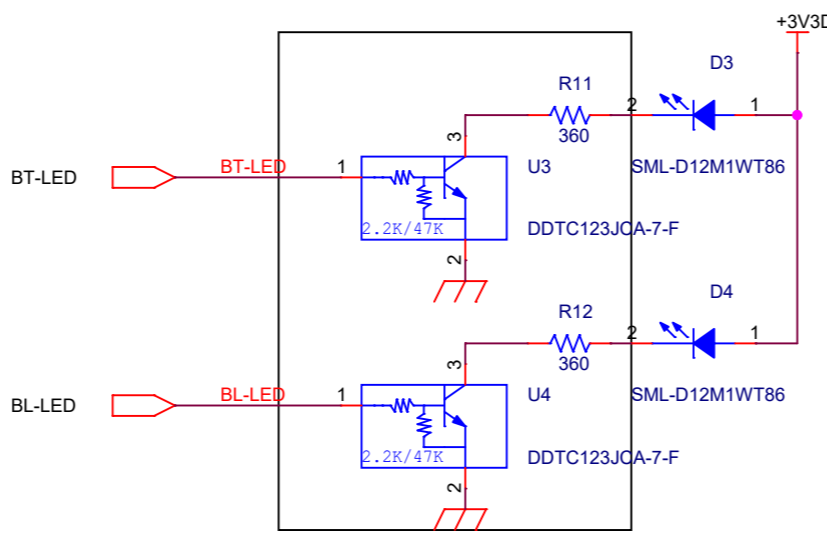
A

SFPから遠くでよい

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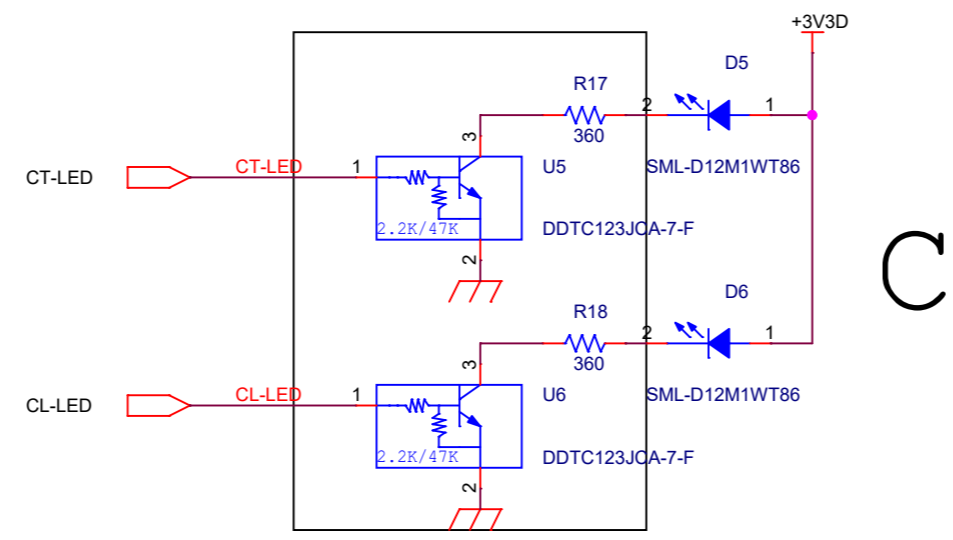
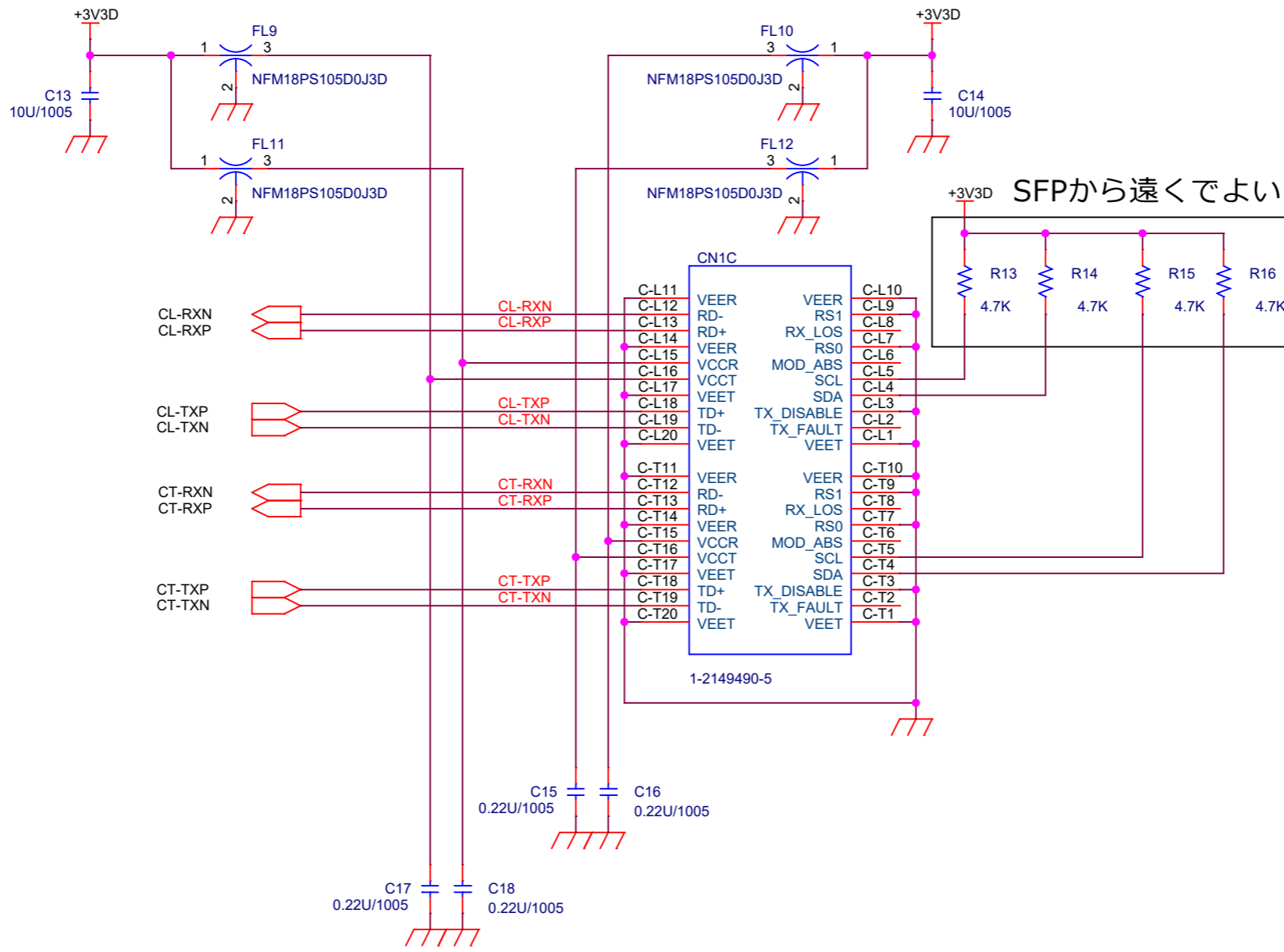
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B

SFPから遠くでよい

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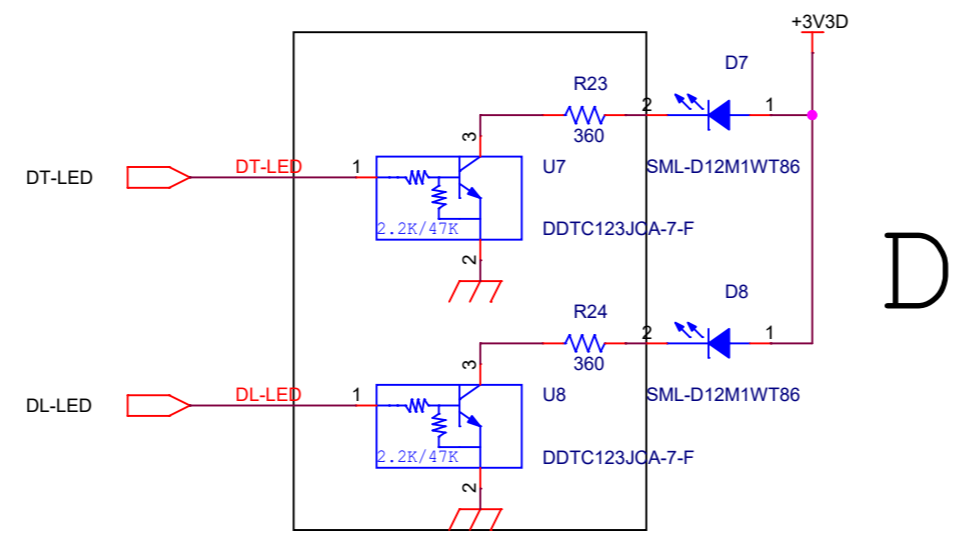
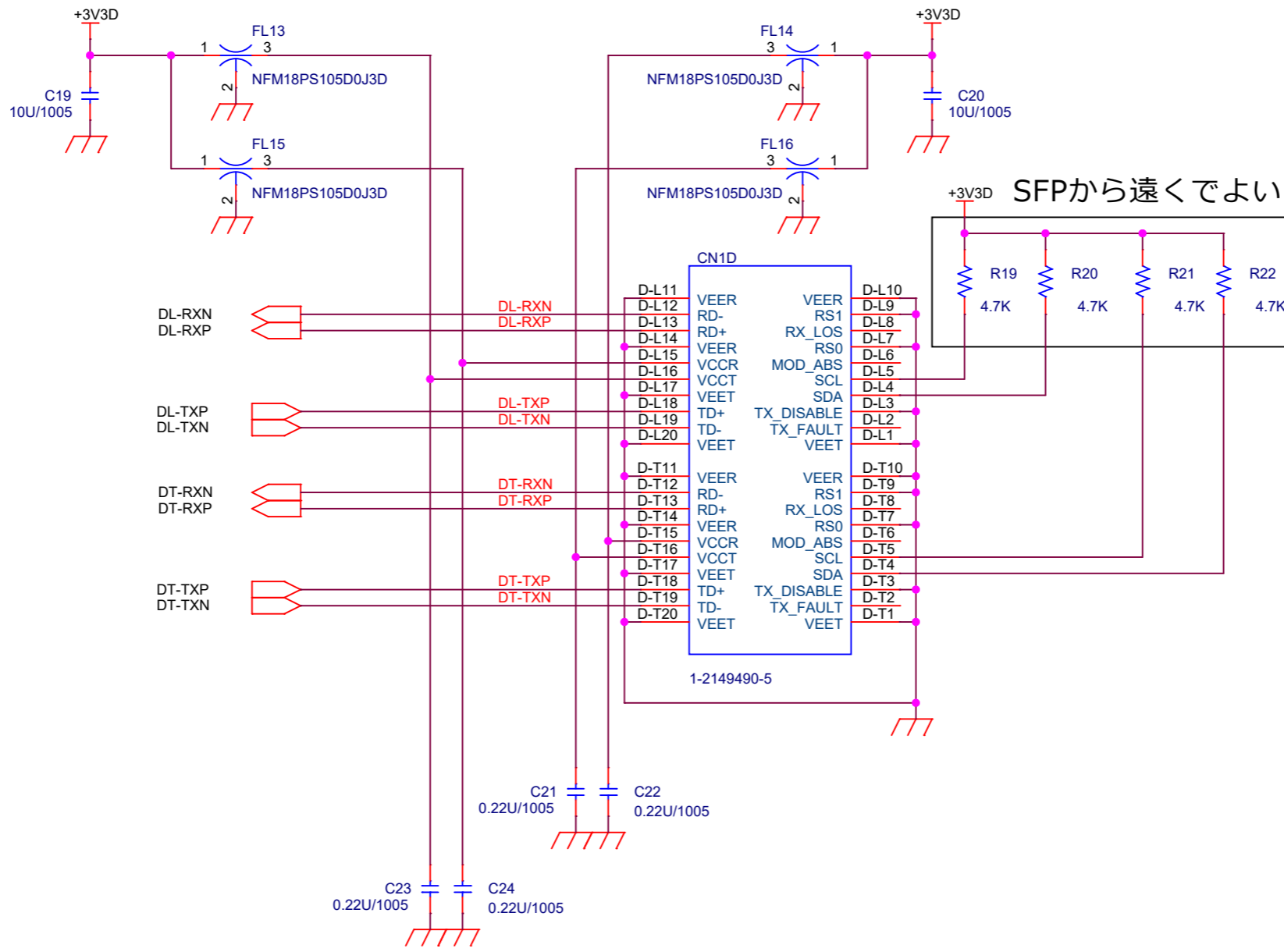


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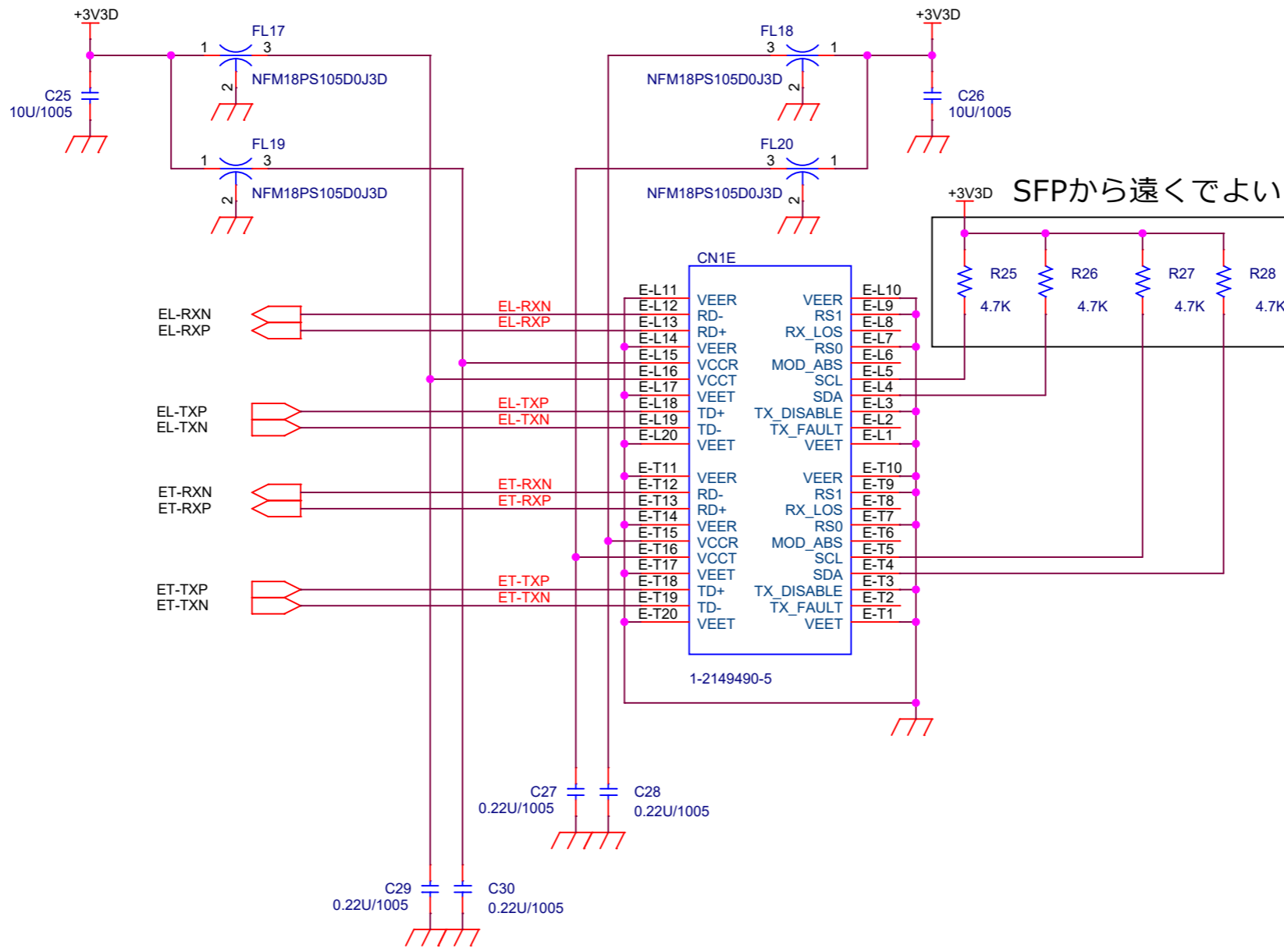
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C

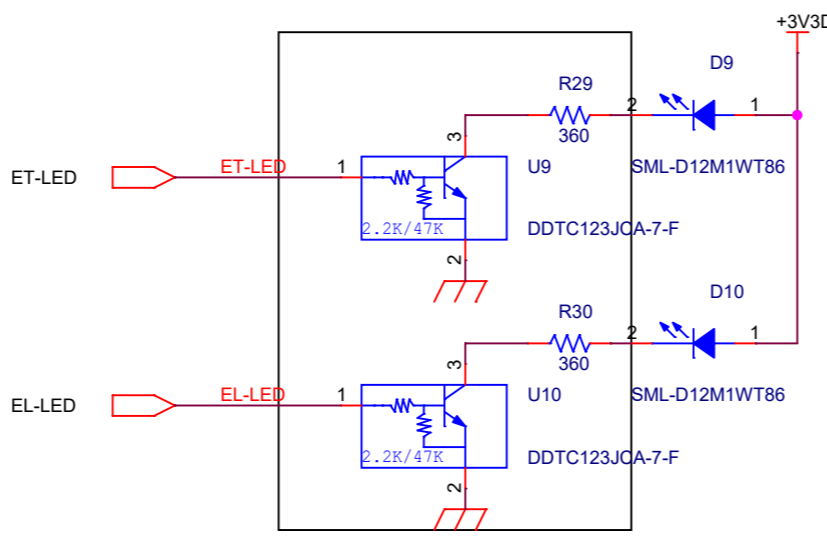
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Title		
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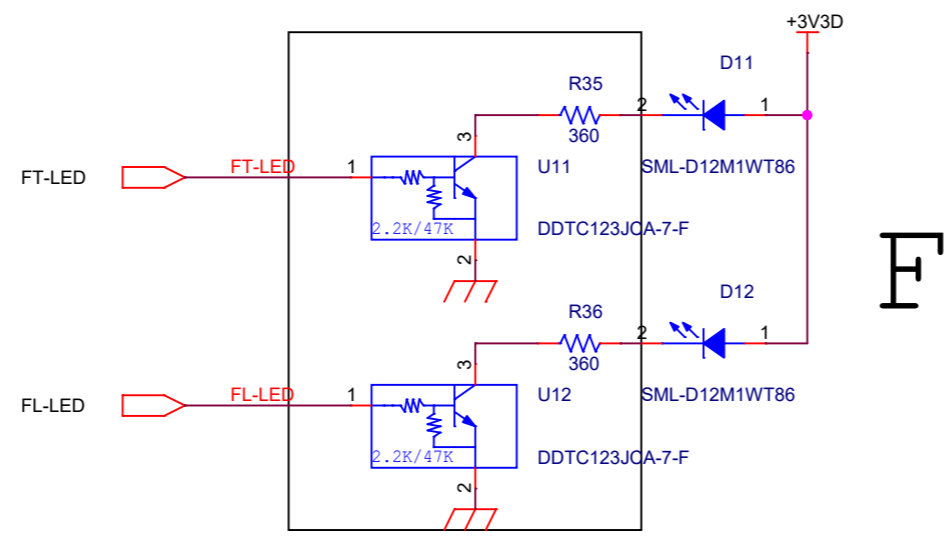
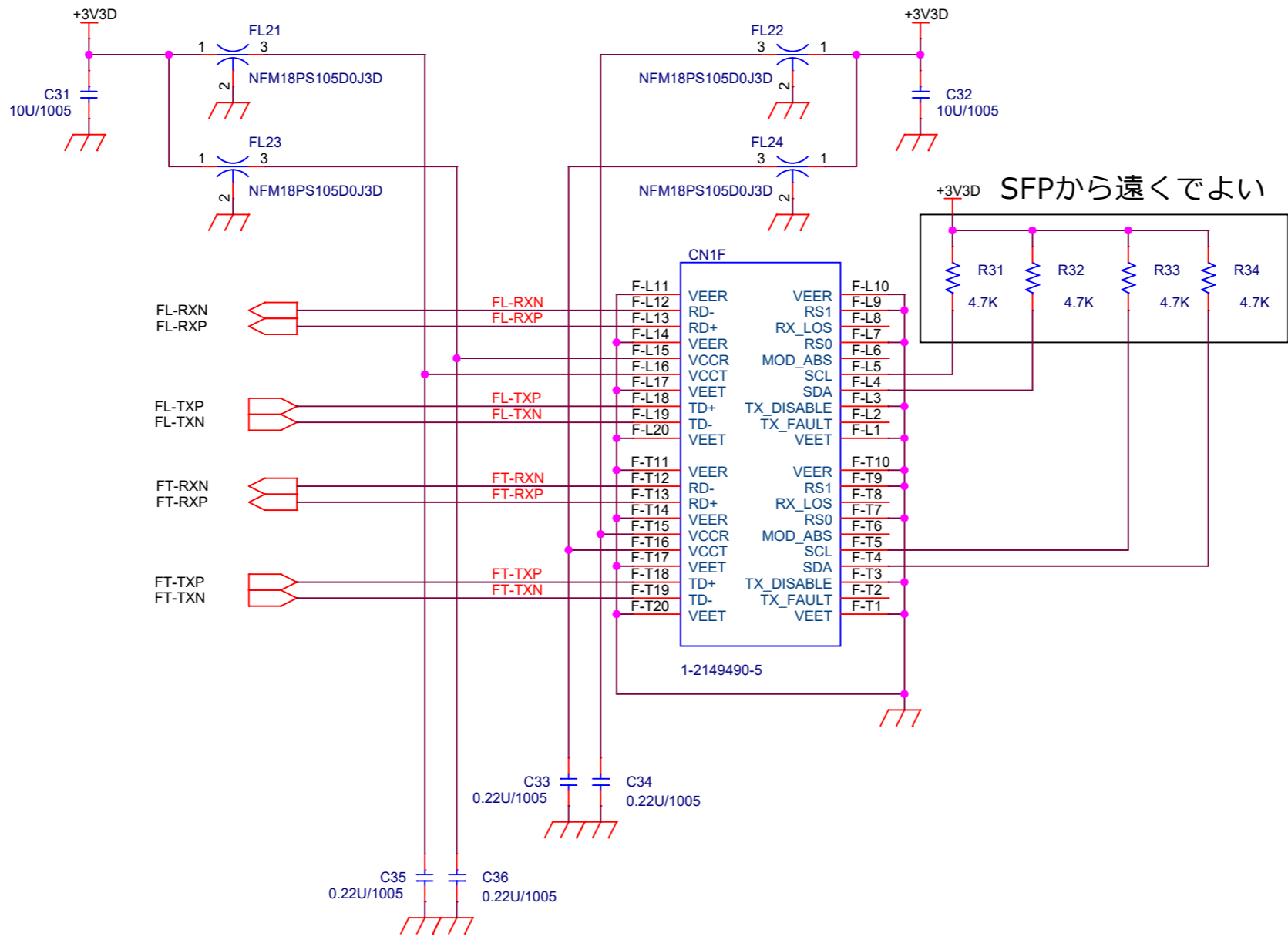
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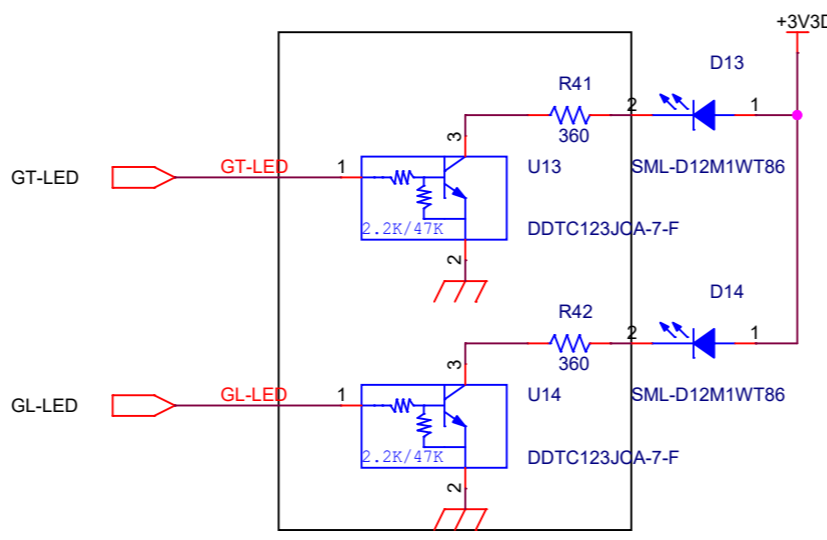
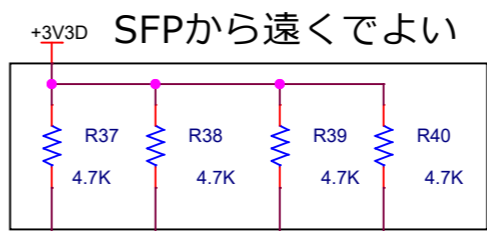
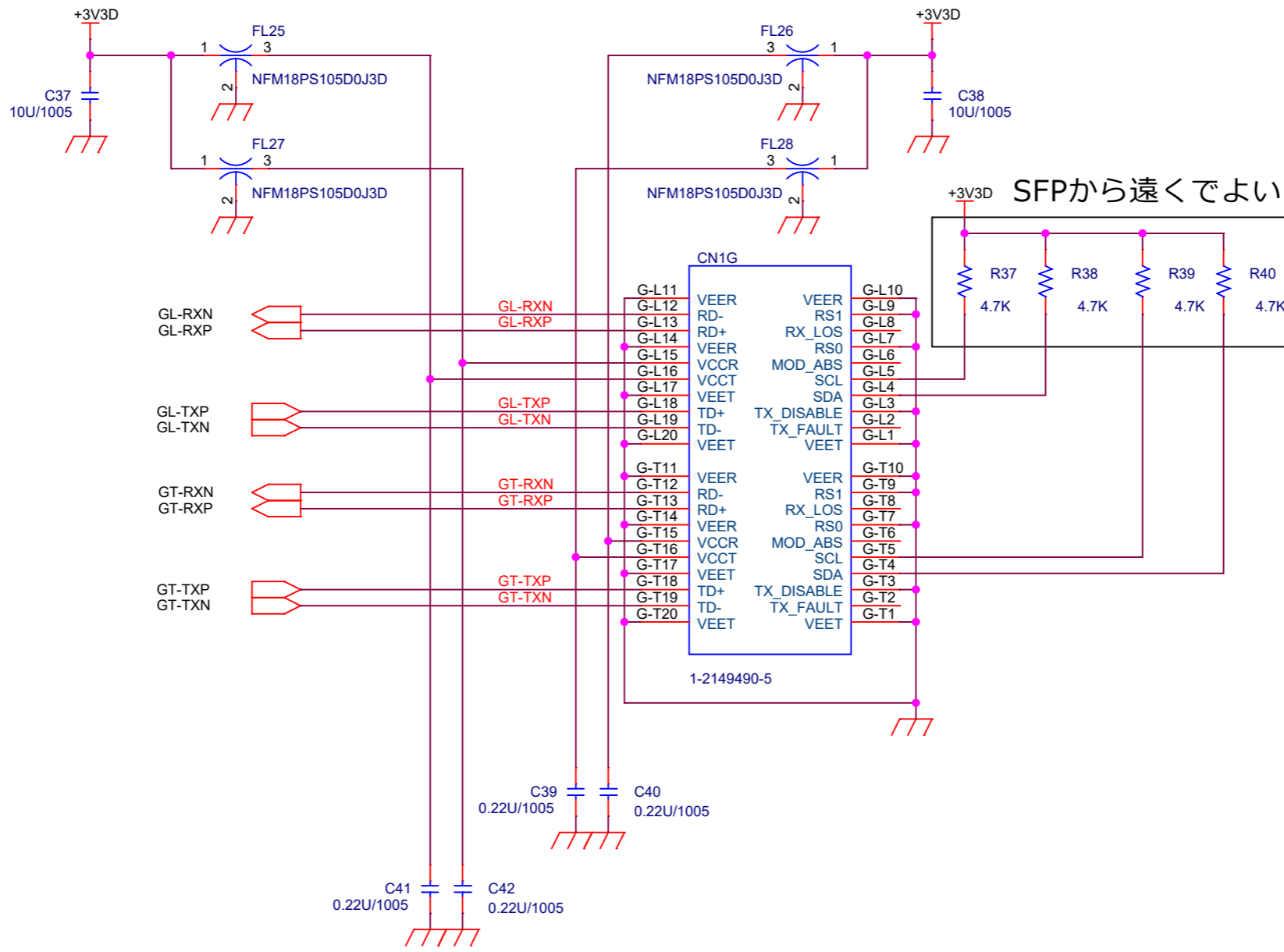
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SFP-E		
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SFP-F		
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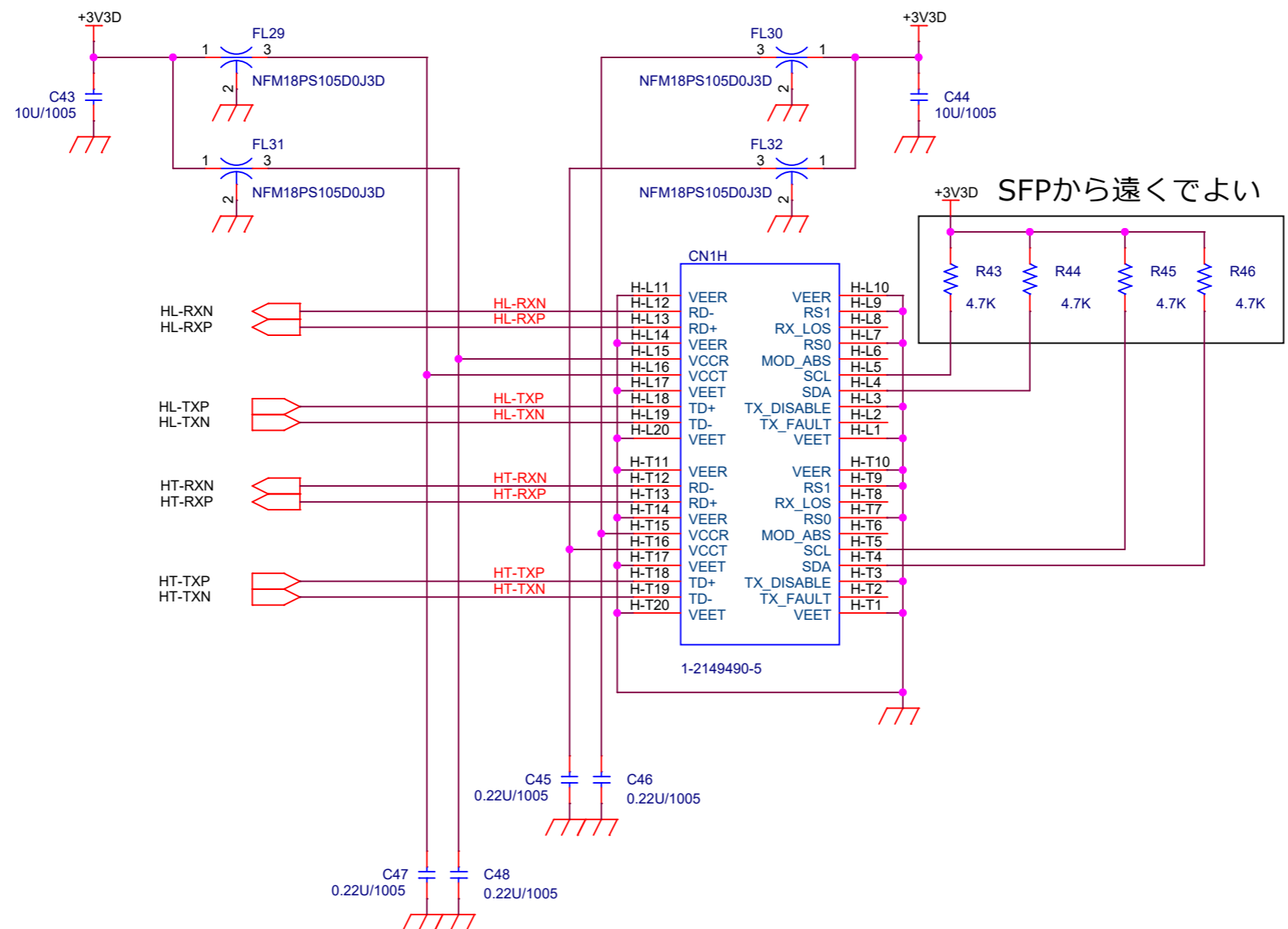




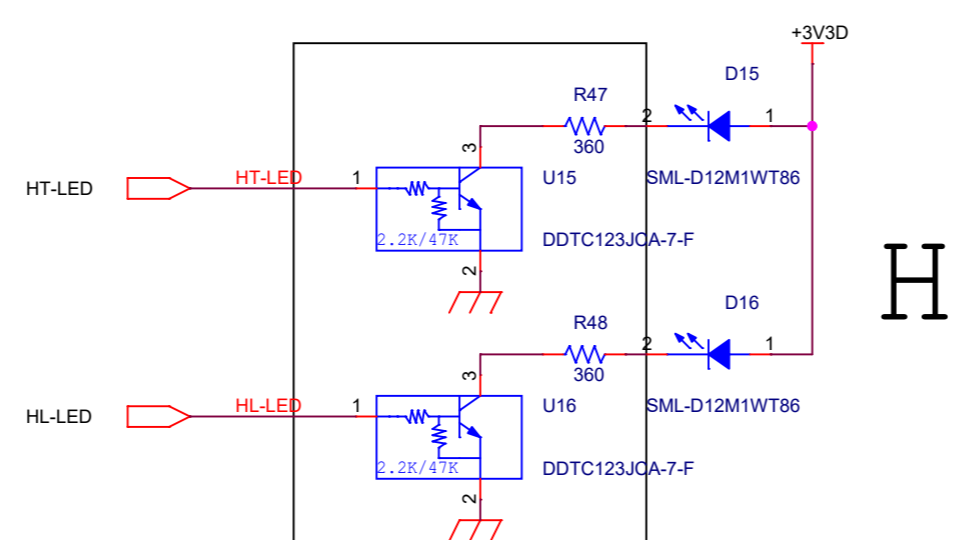
SFPから遠くでよい

G

Title		
SFP-G		
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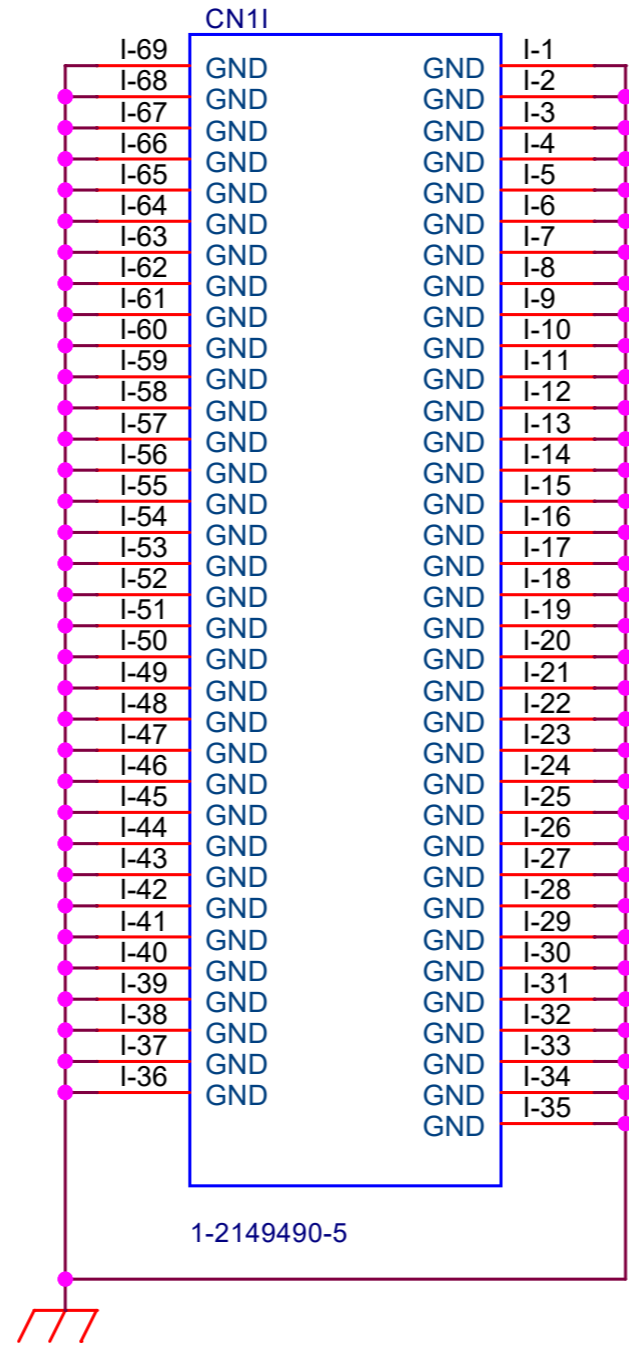
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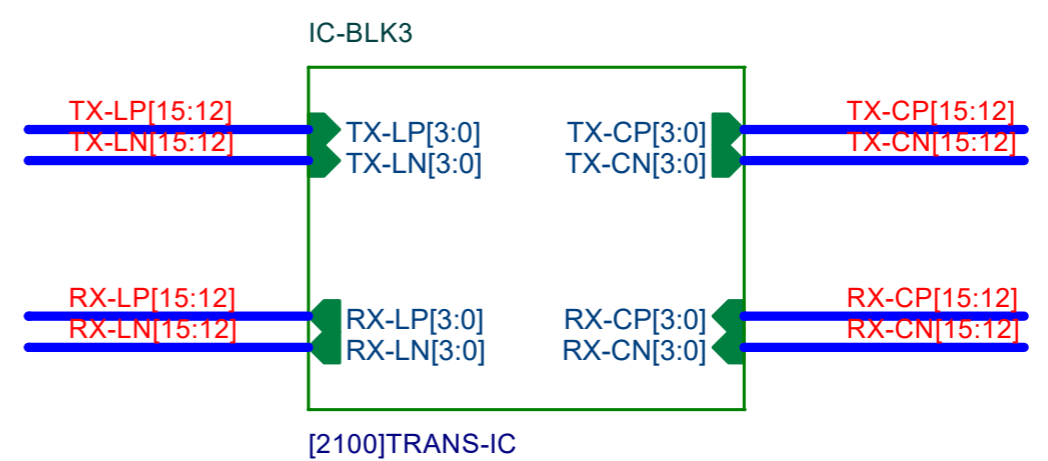
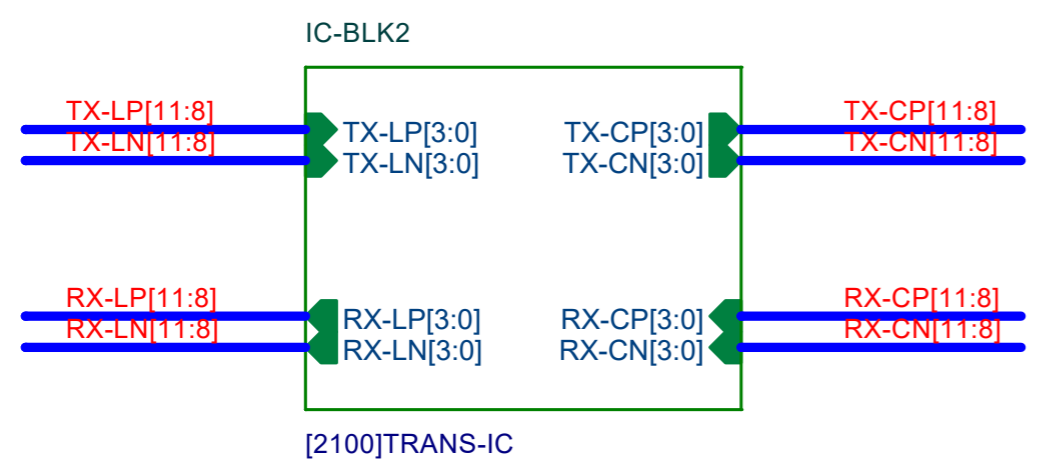
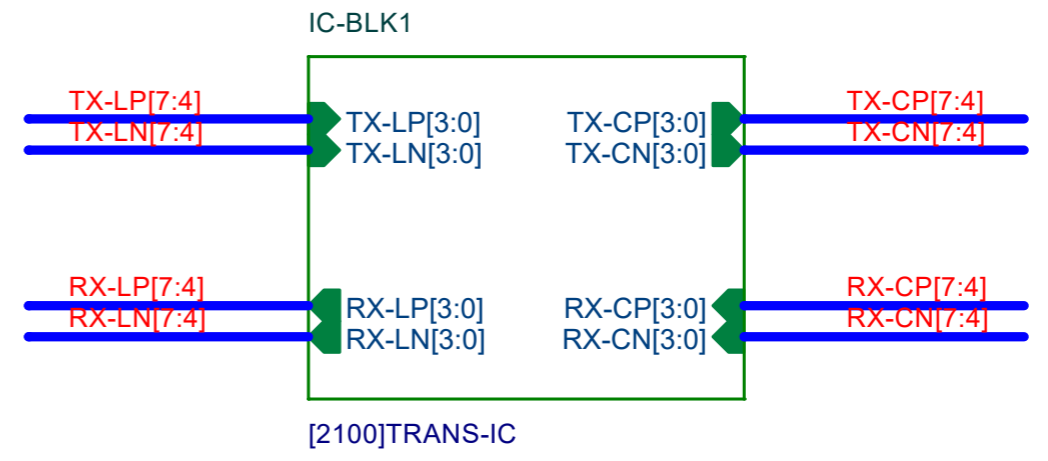
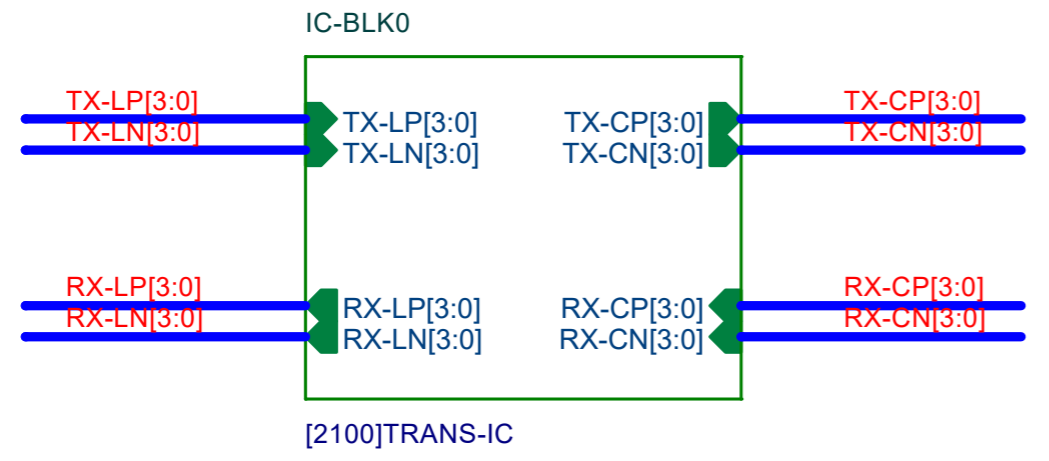
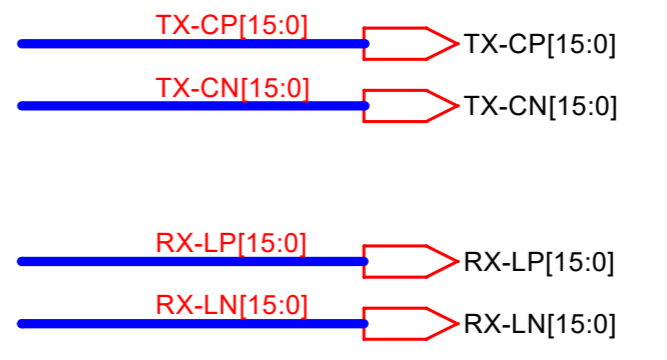
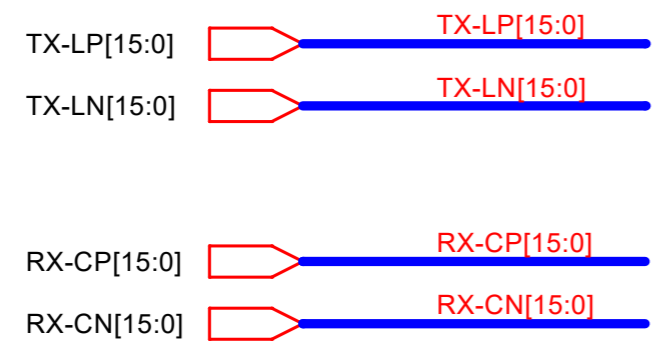
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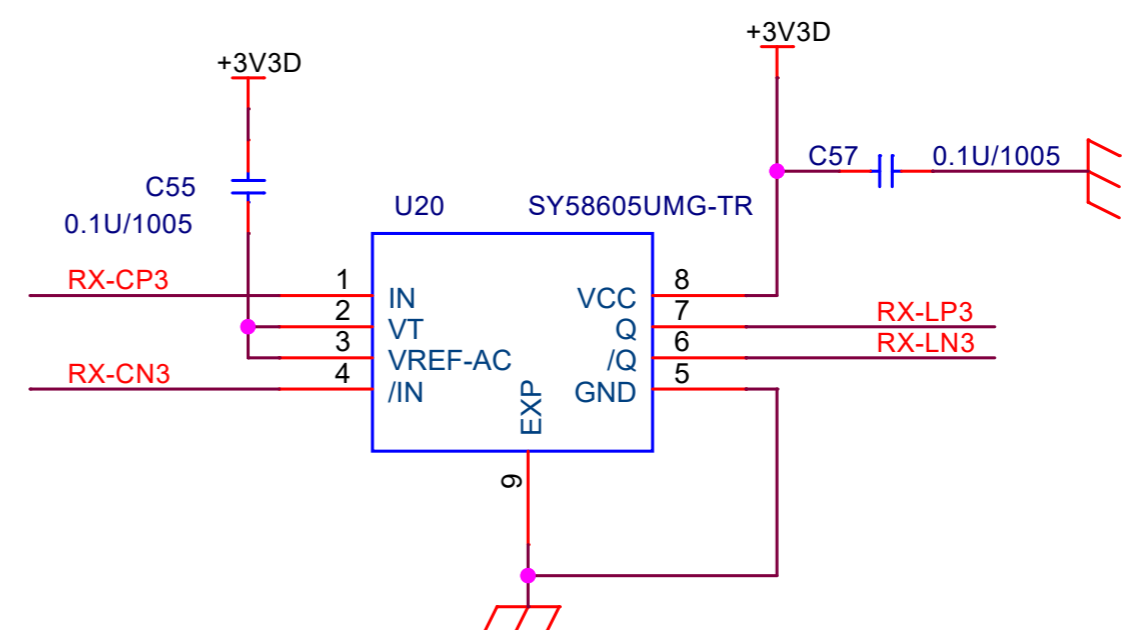
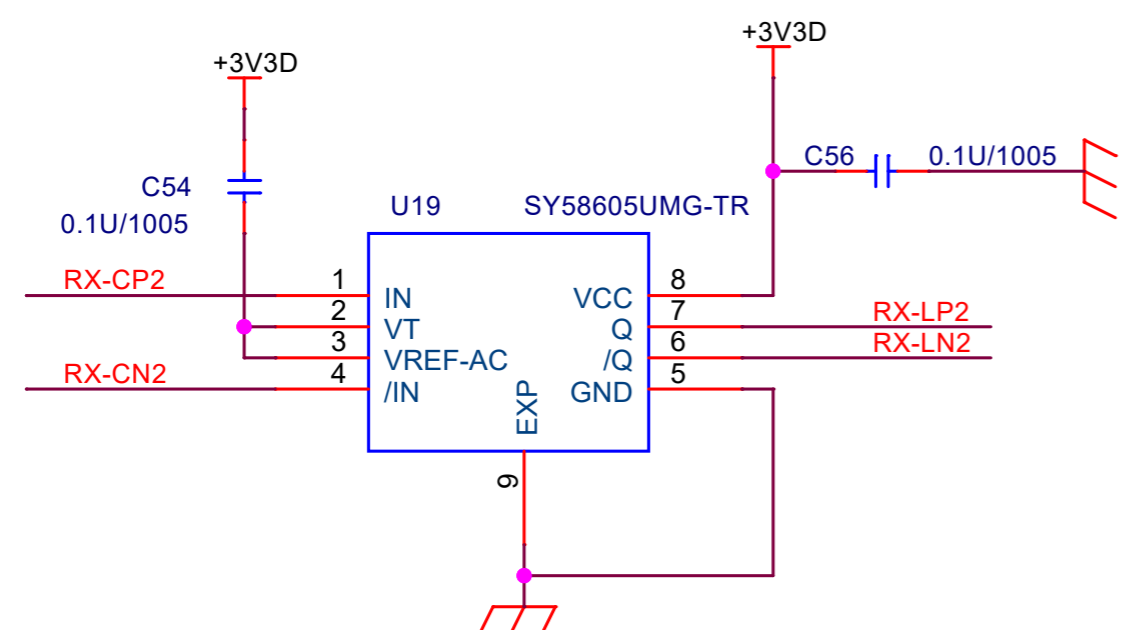
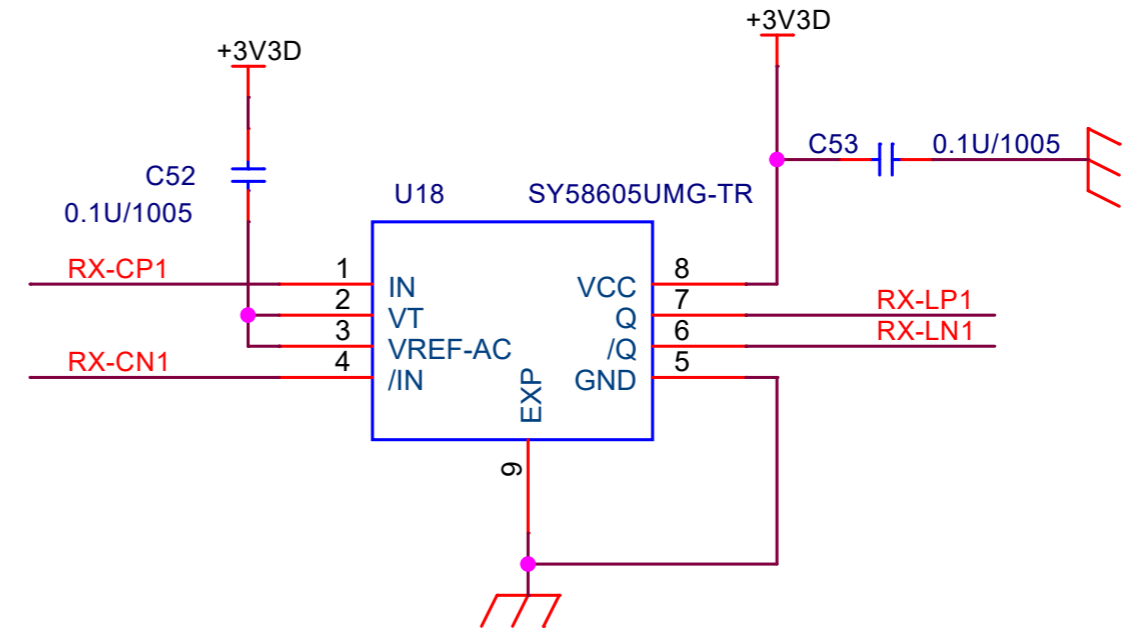
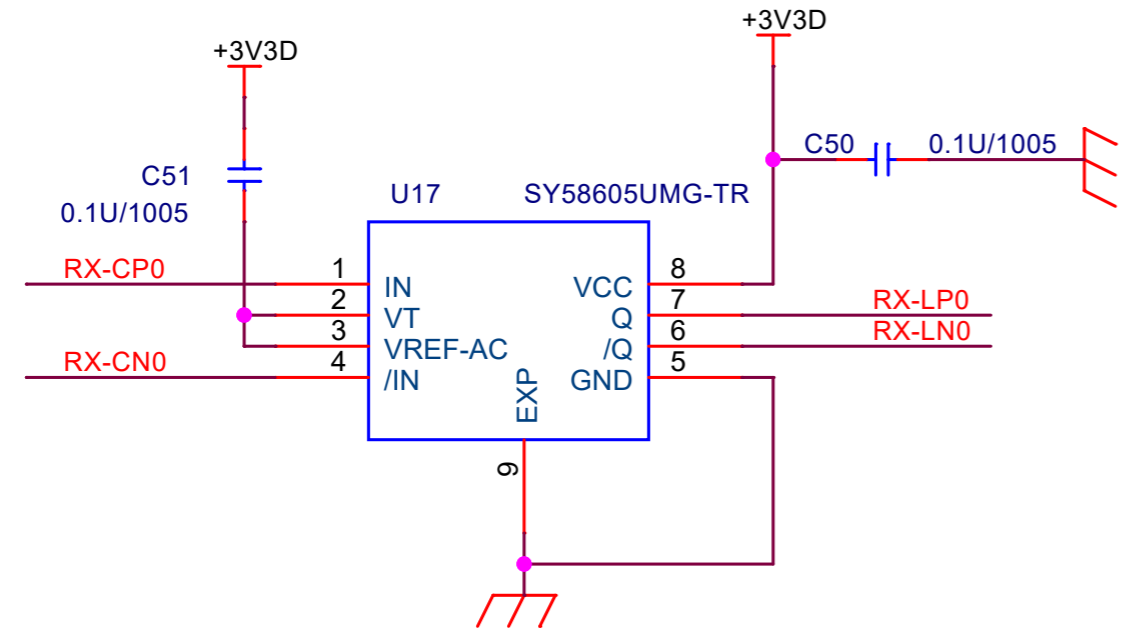
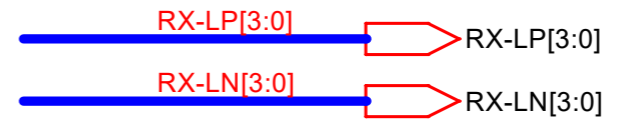
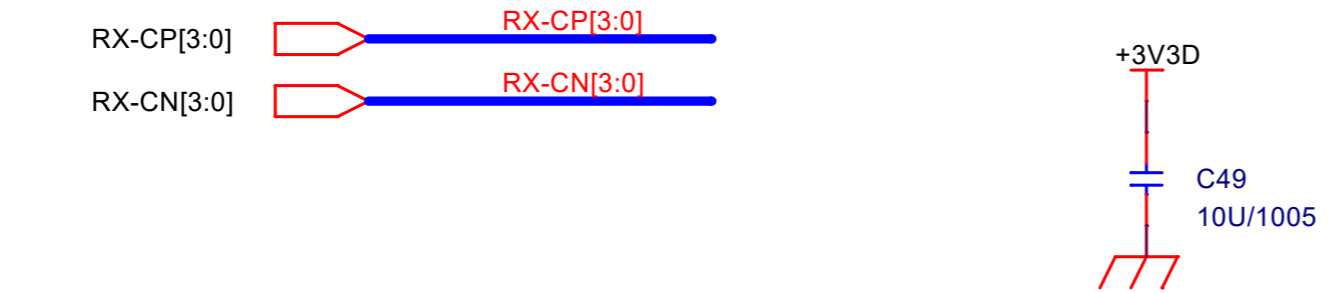
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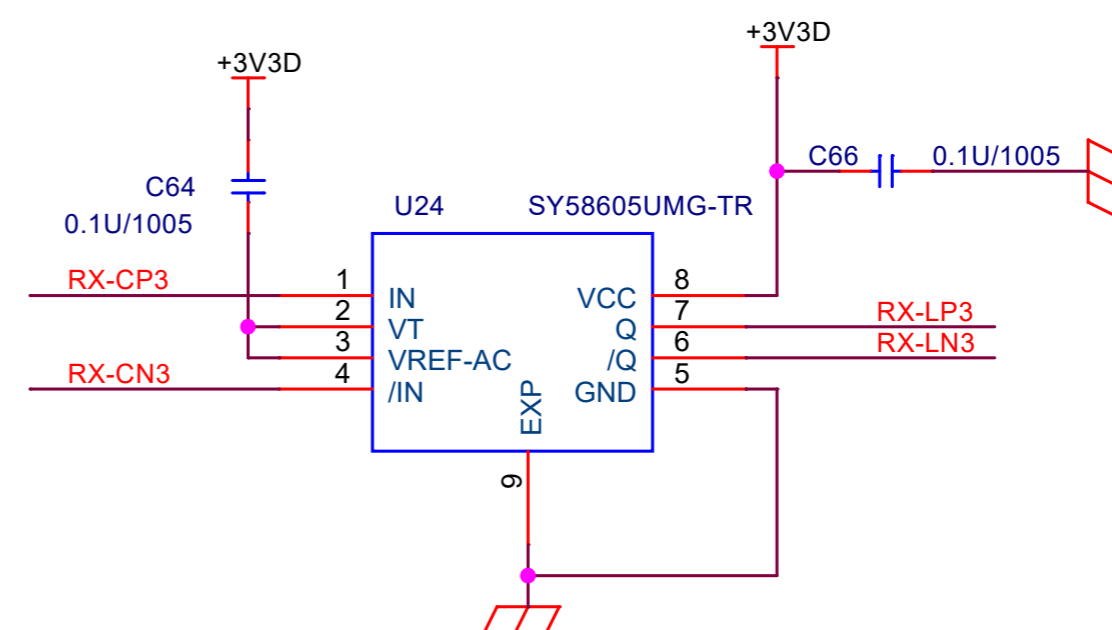
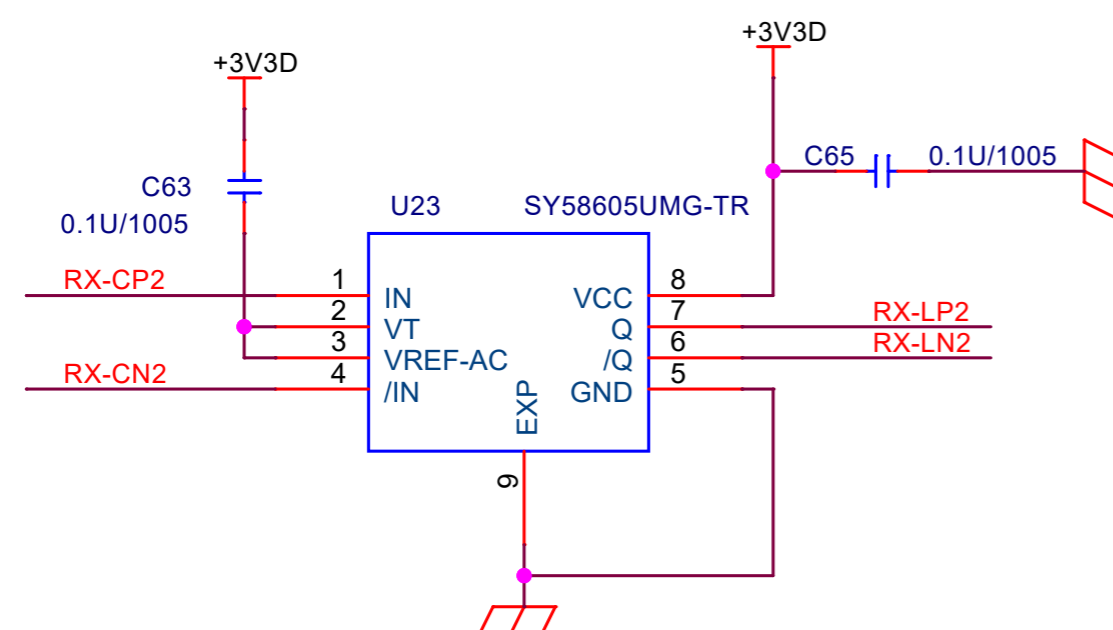
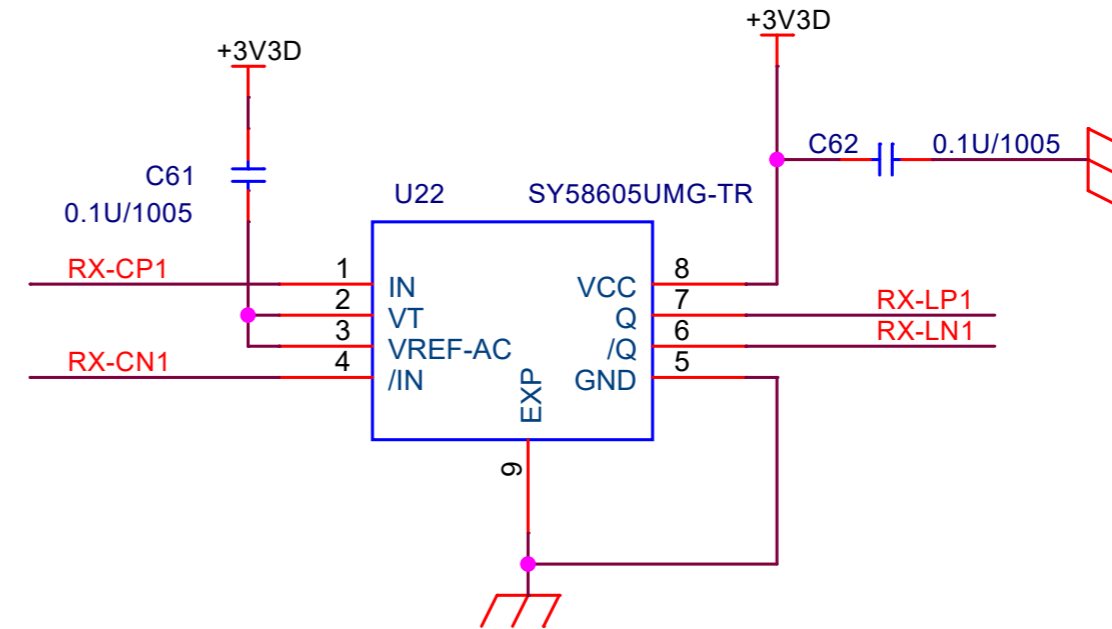
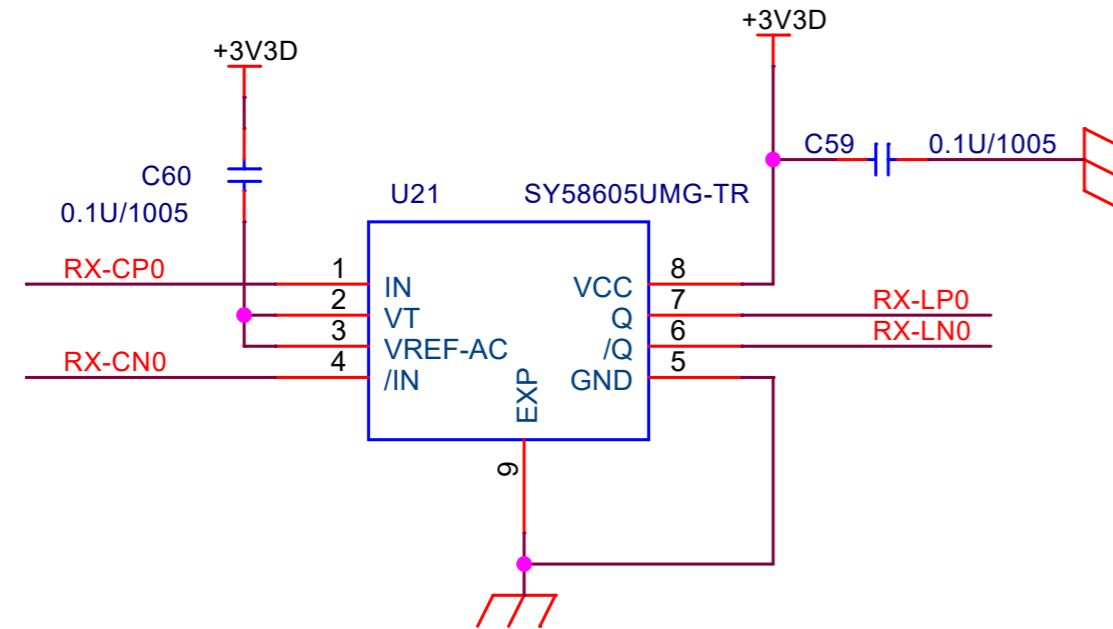
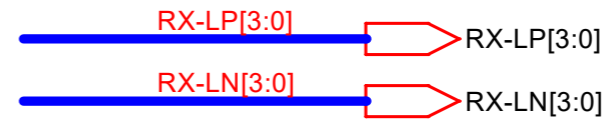
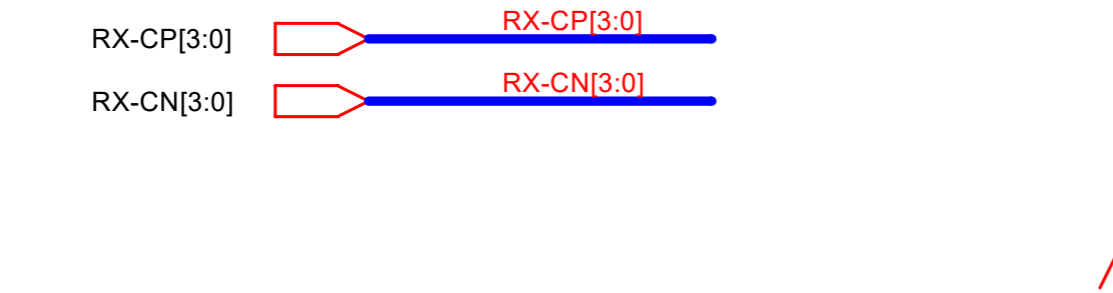


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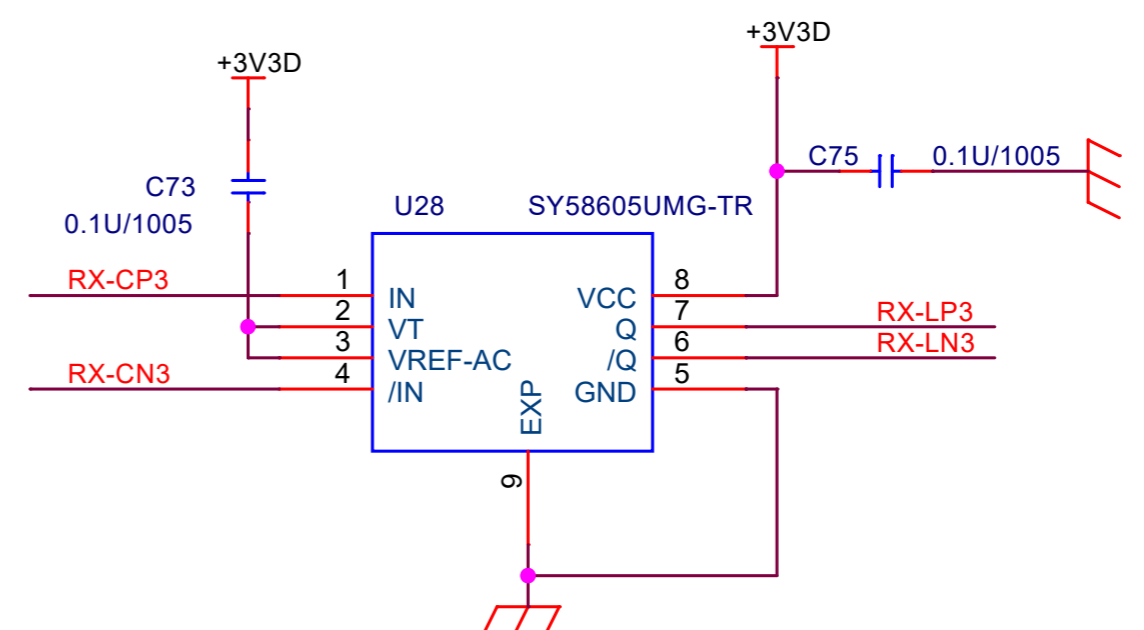
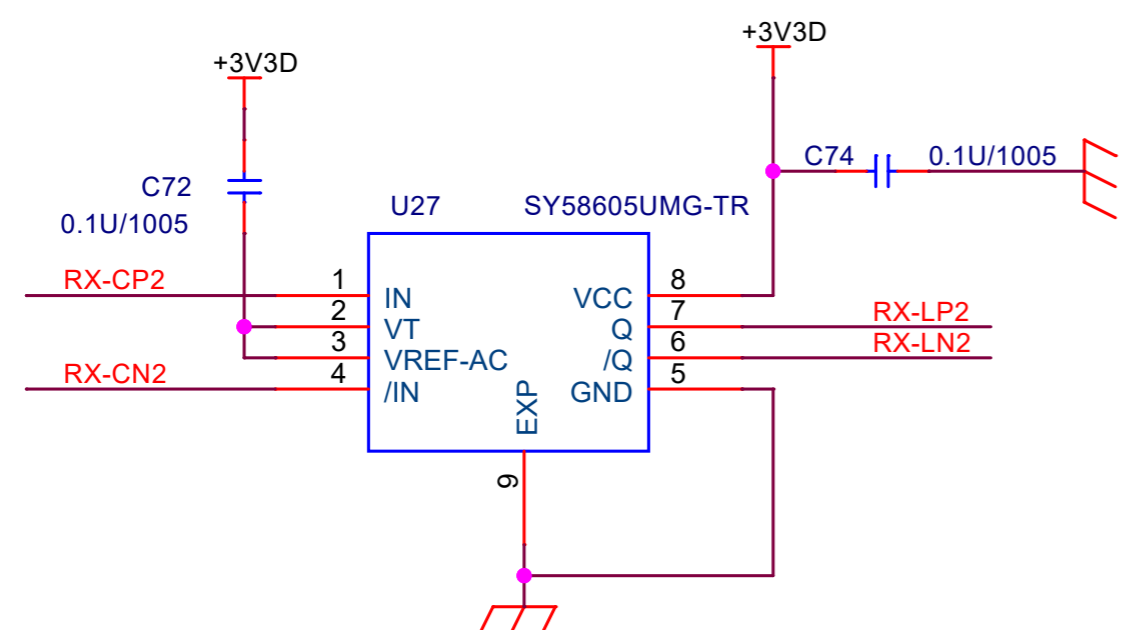
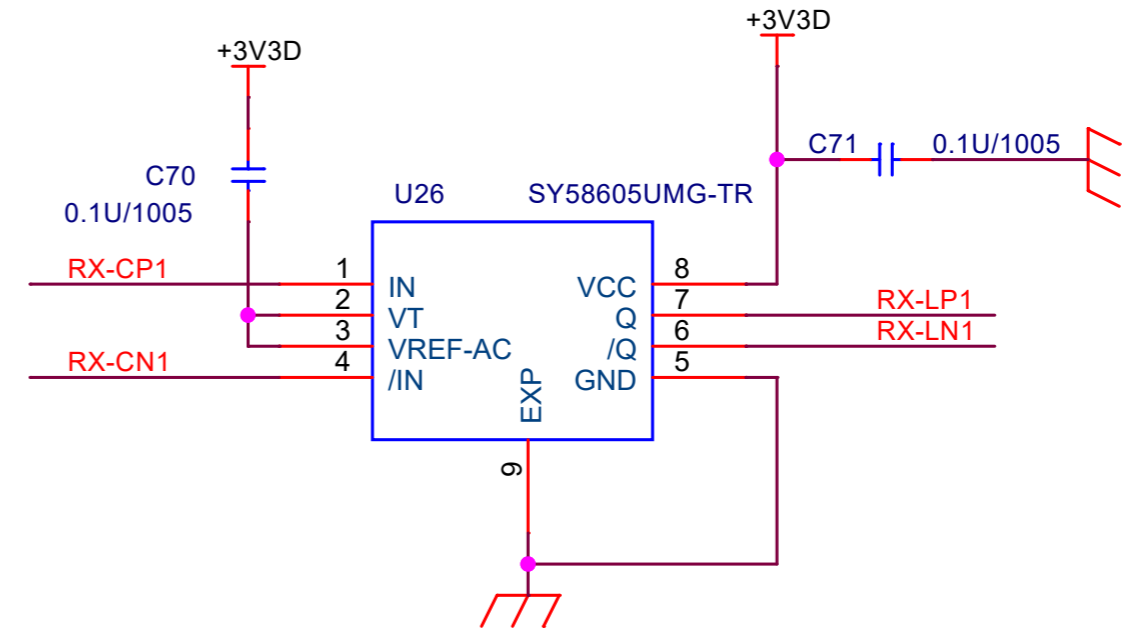
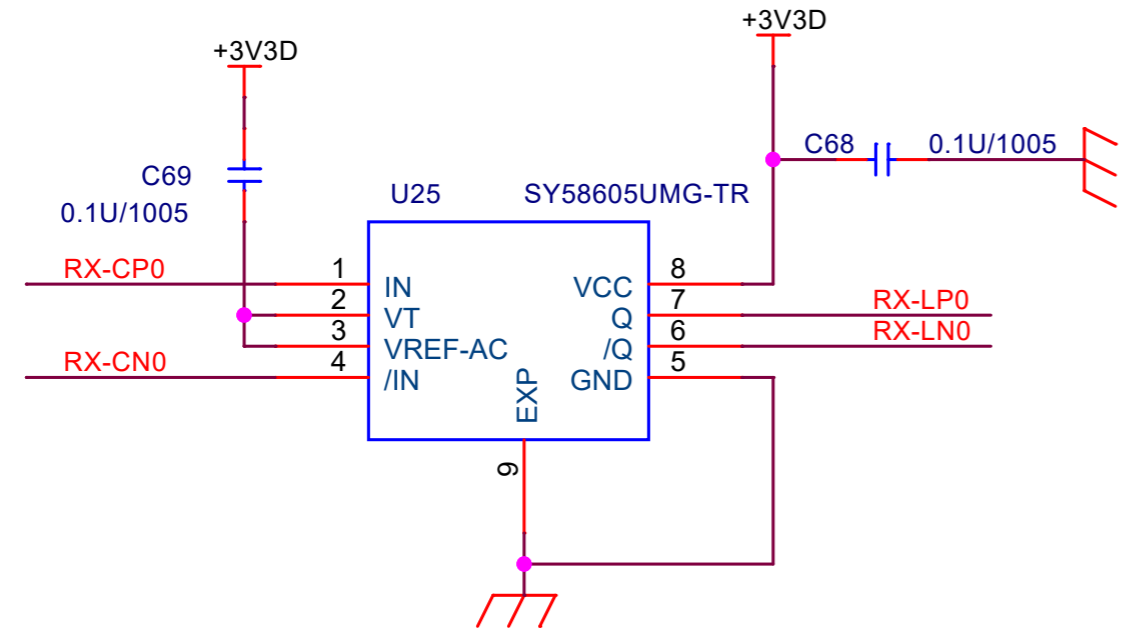
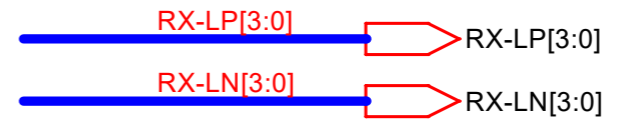
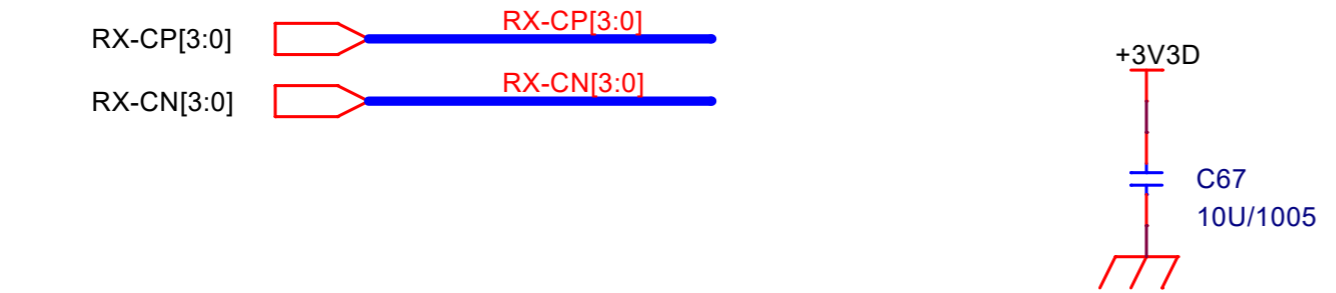
# CML-to-LVDS

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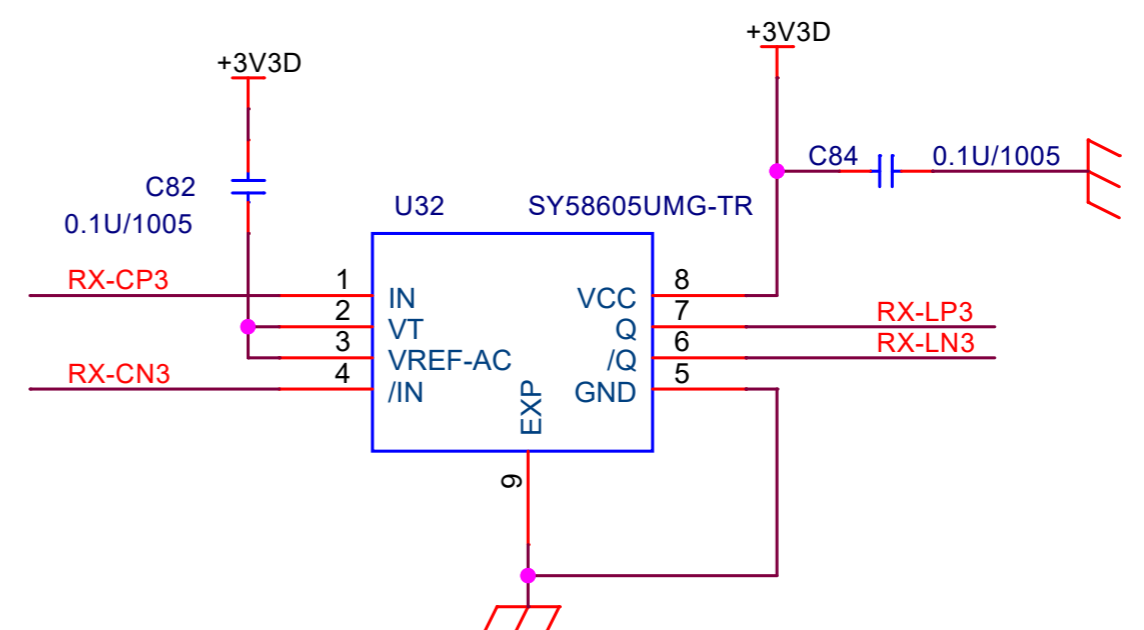
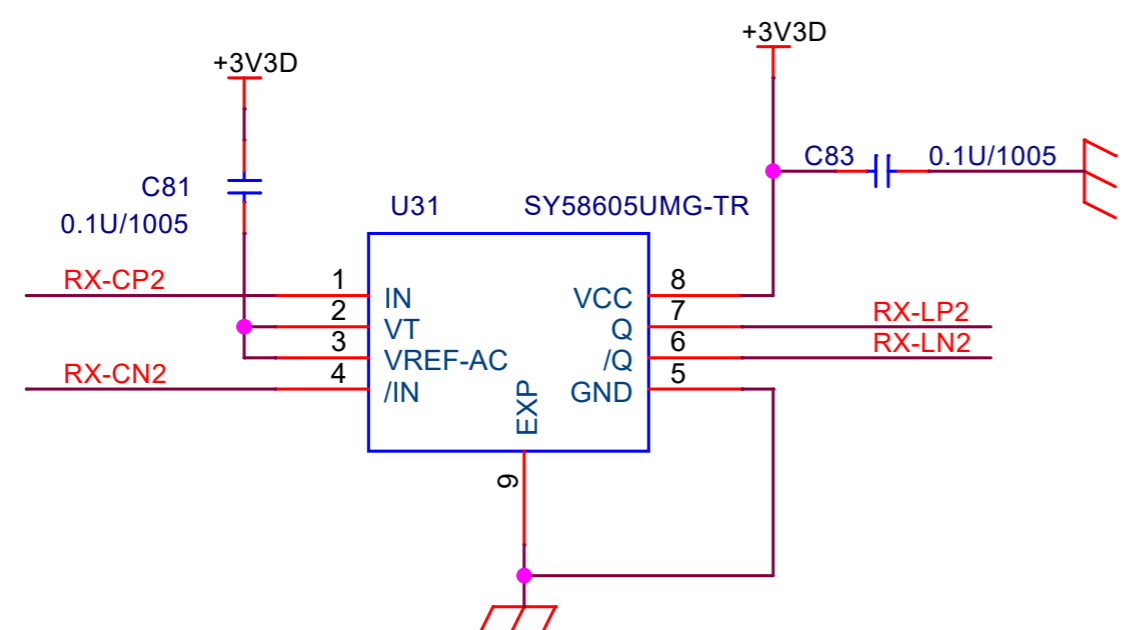
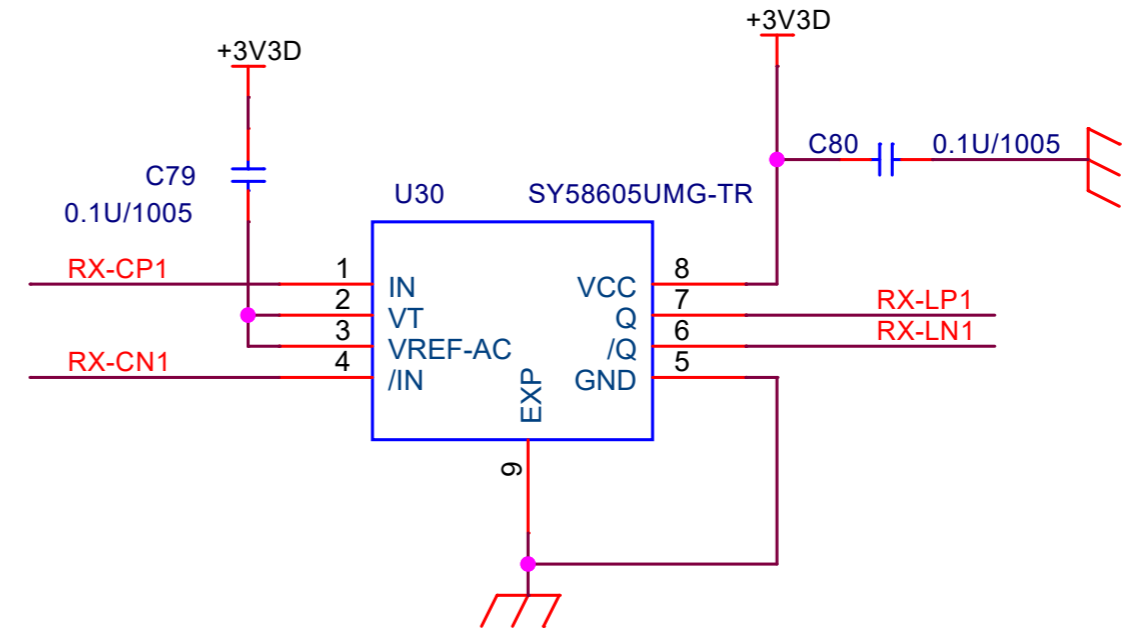
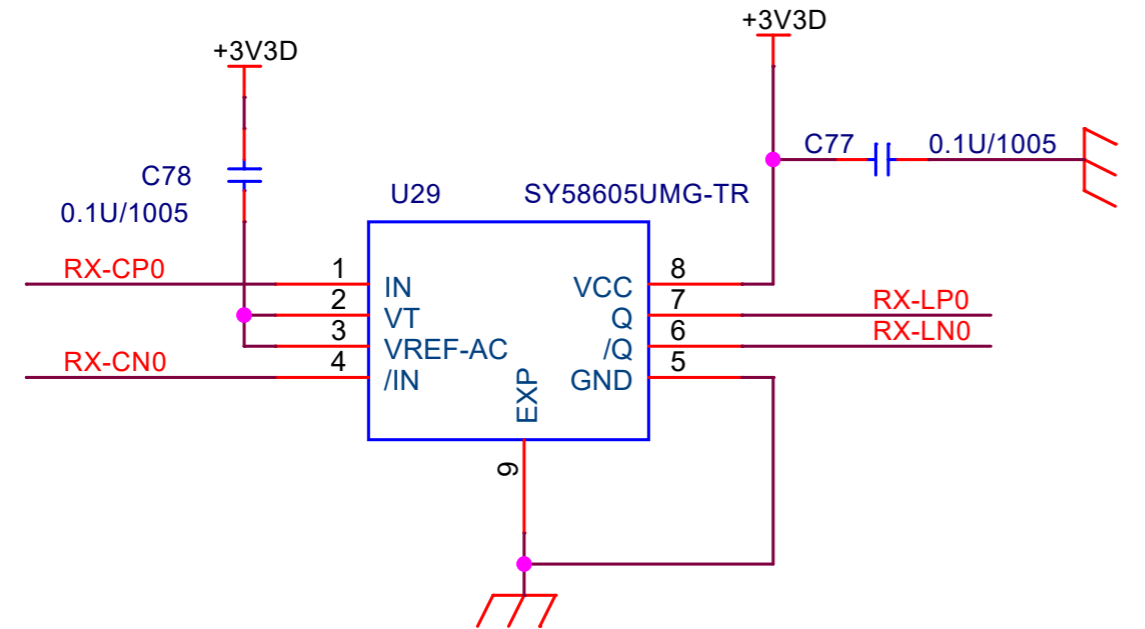
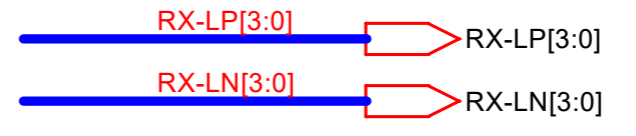
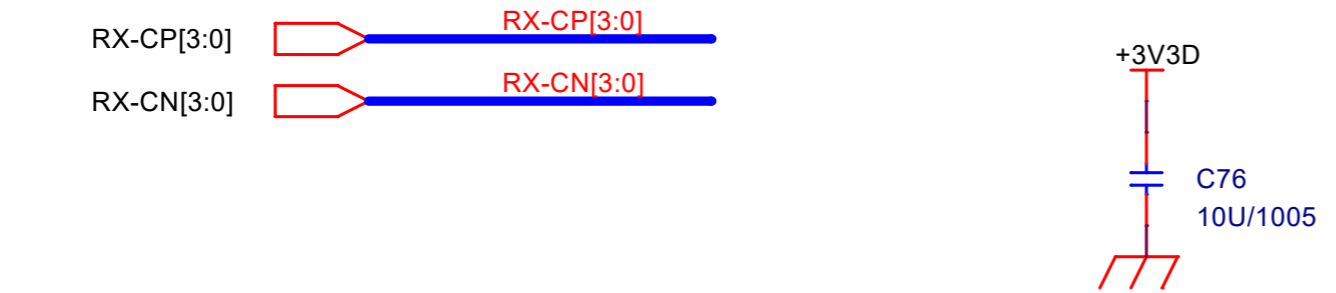
# CML-to-LVDS

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# CML-to-LVDS

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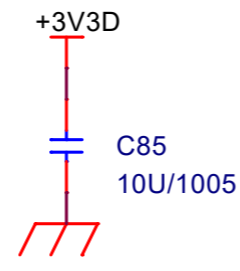


CML-to-LVDS

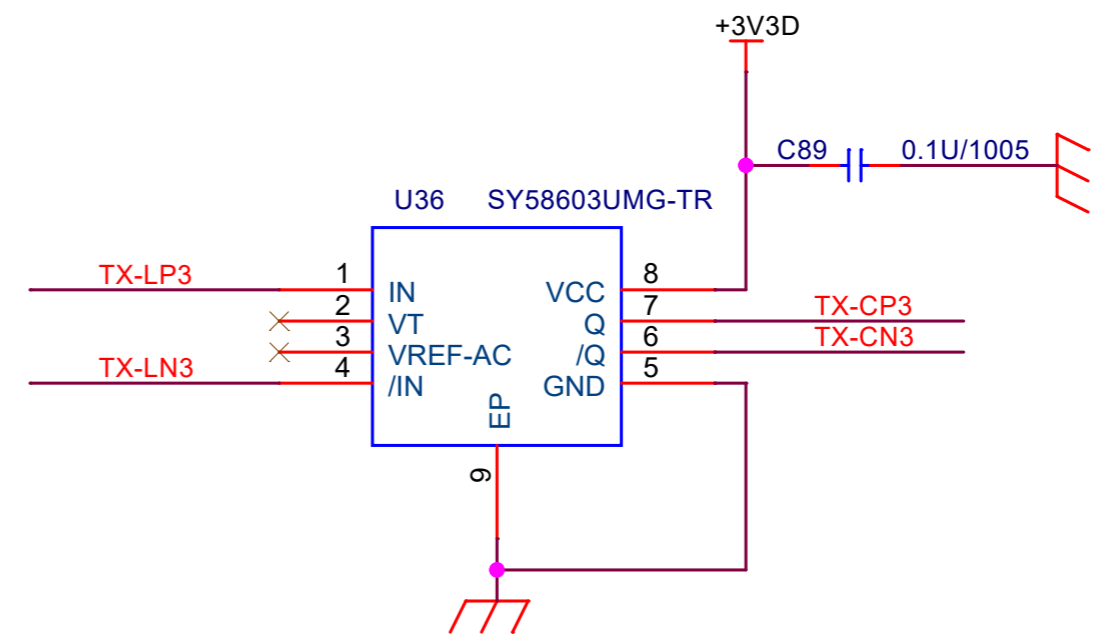
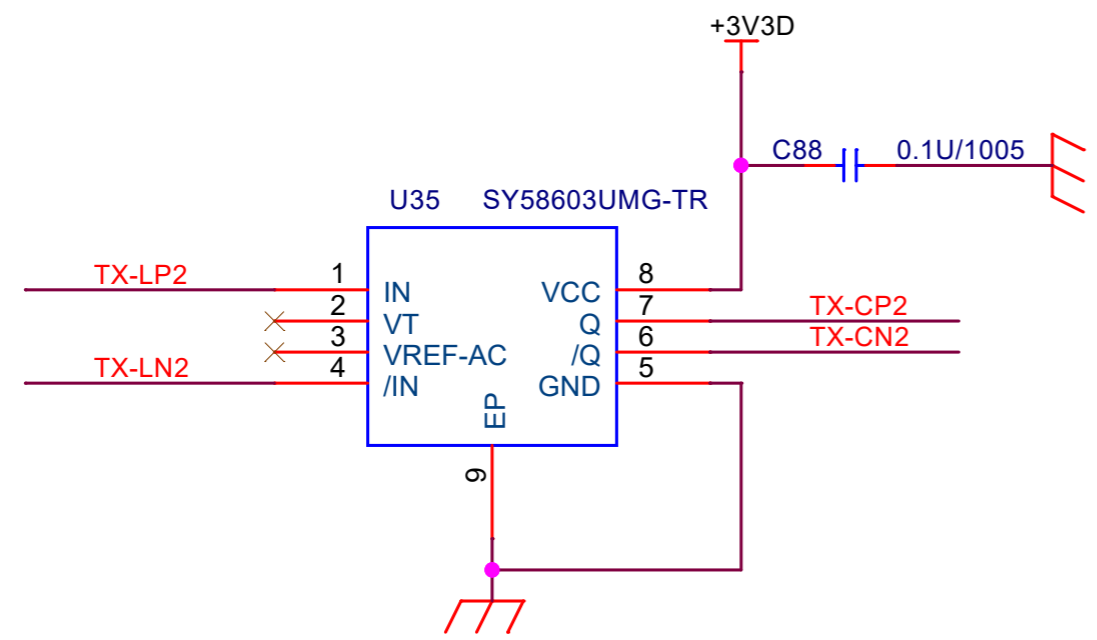
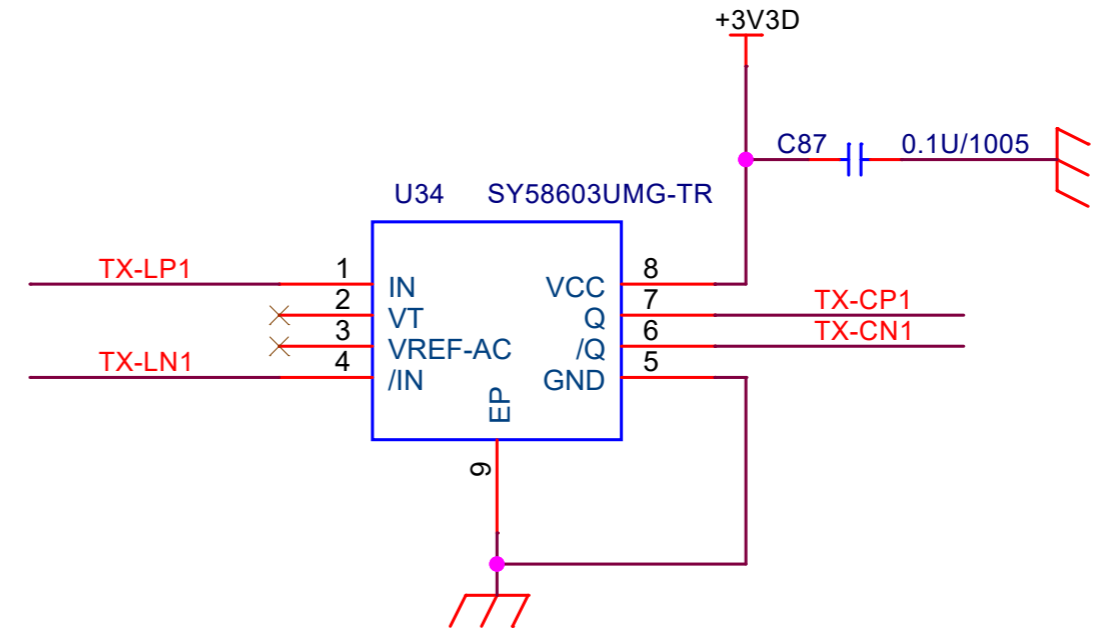
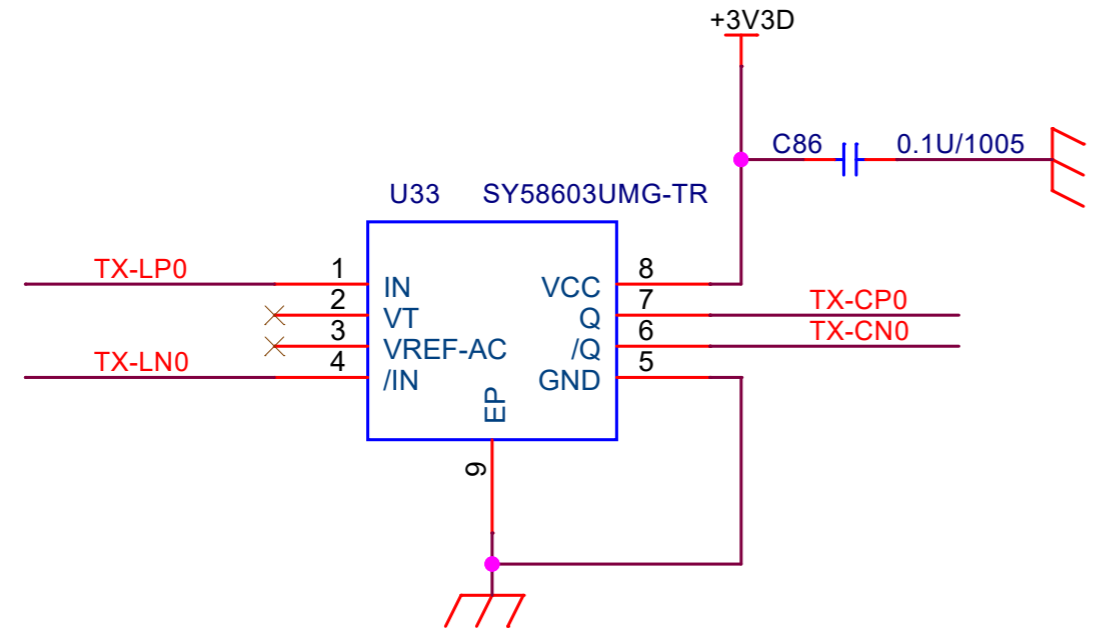
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TX-LP[3:0]  
TX-LN[3:0]



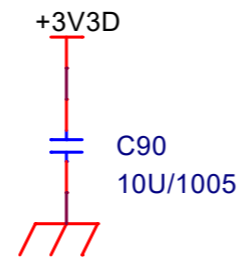
TX-CP[3:0]  
TX-CN[3:0]



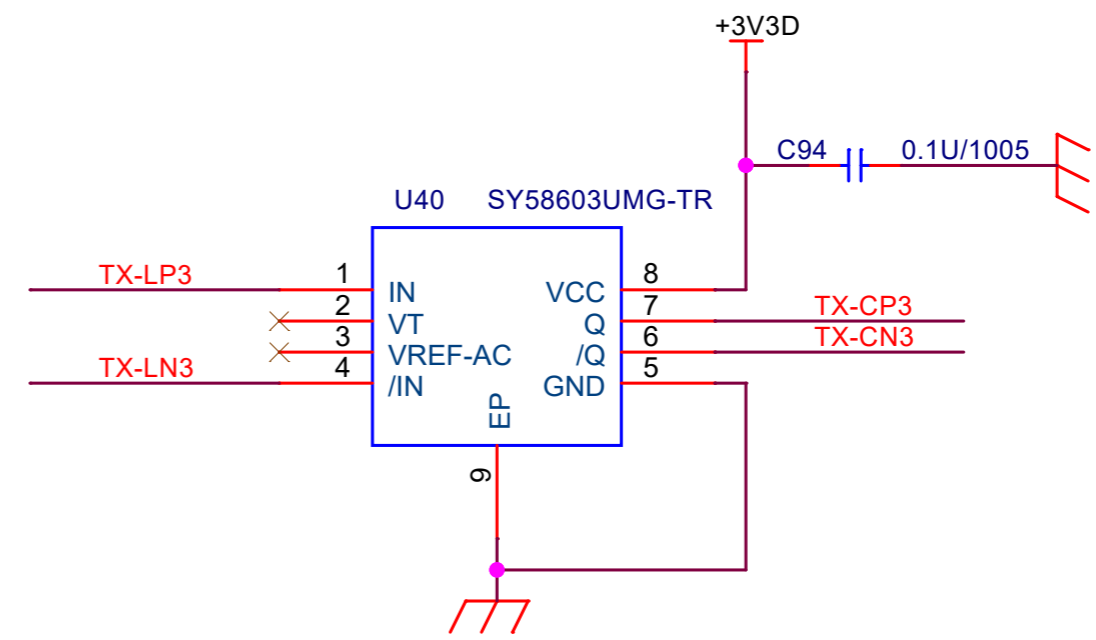
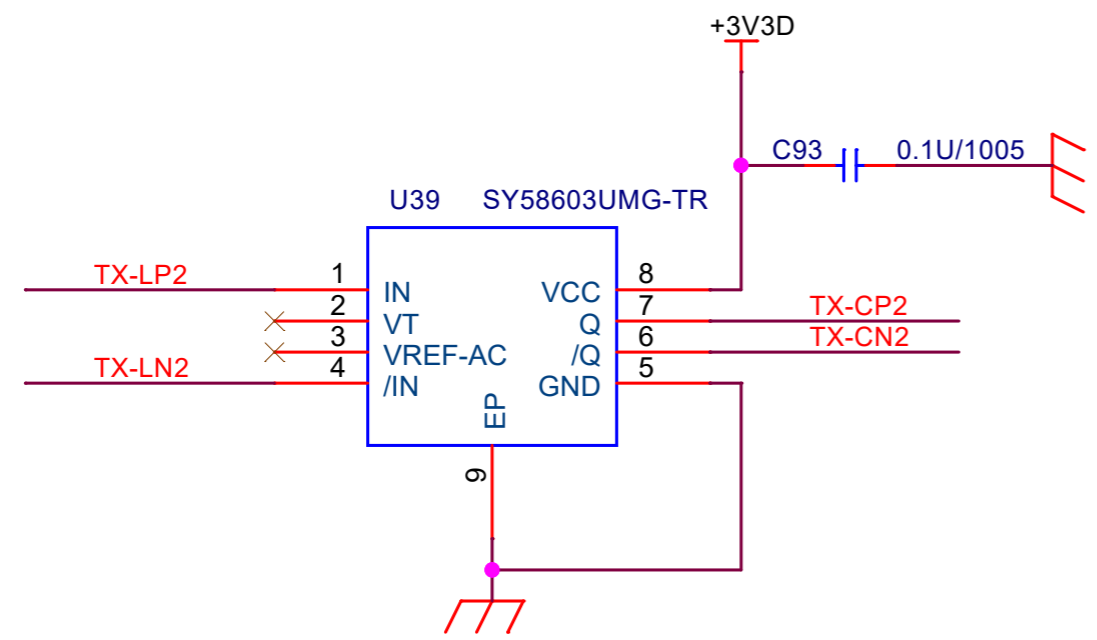
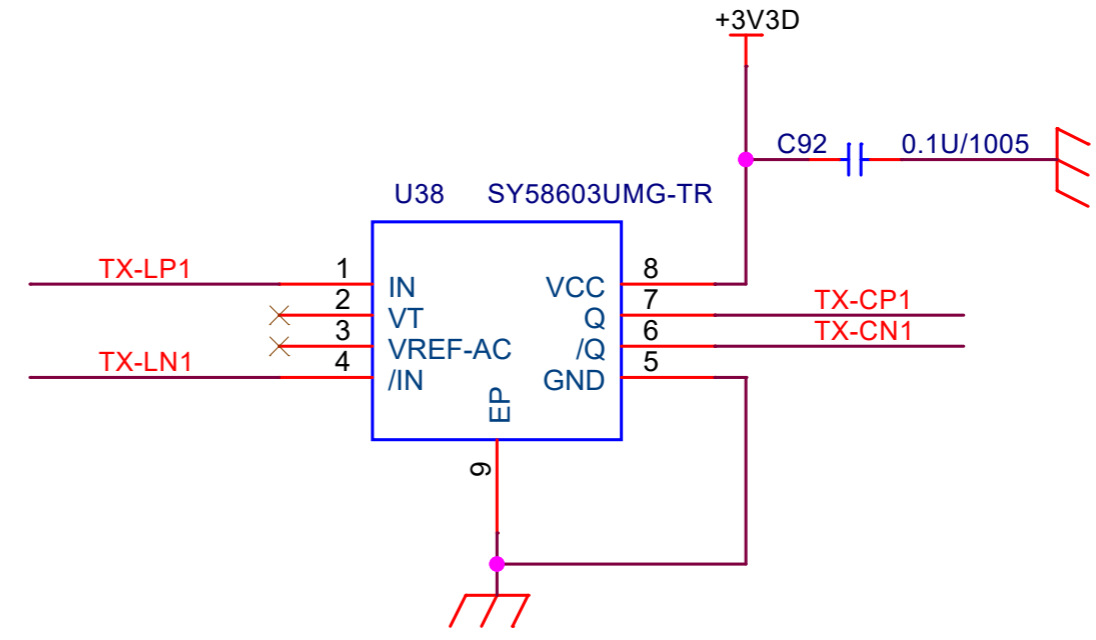
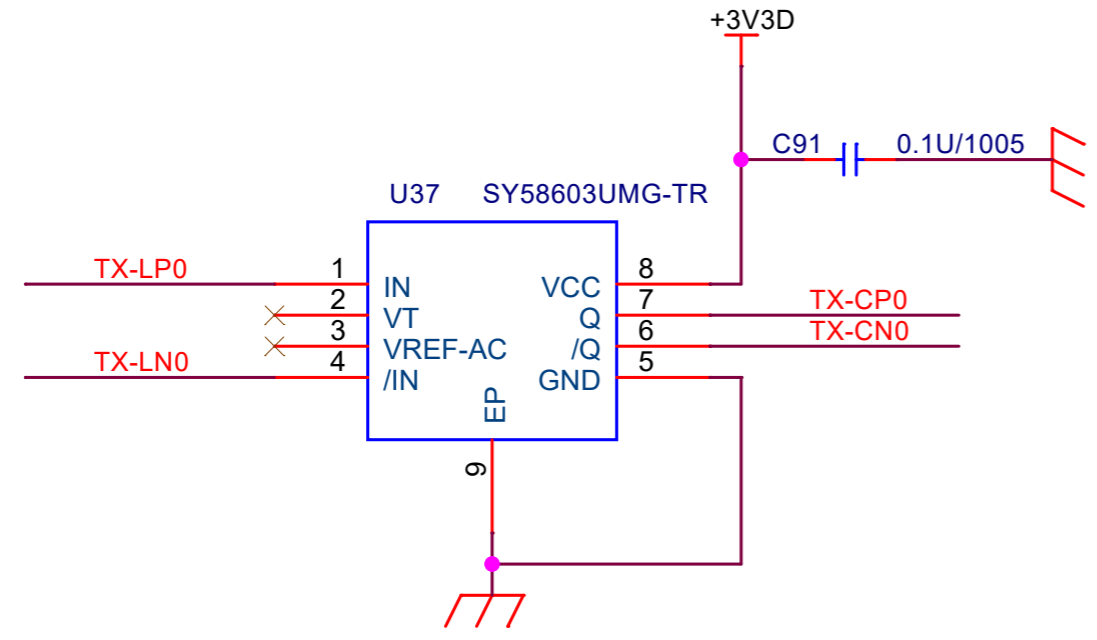
# LVDS-to-CML

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IO standard trans. TX		
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TX-LP[3:0]  
TX-LN[3:0]



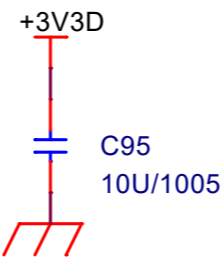
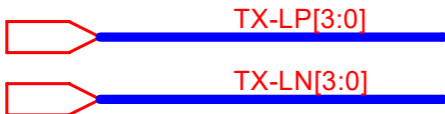
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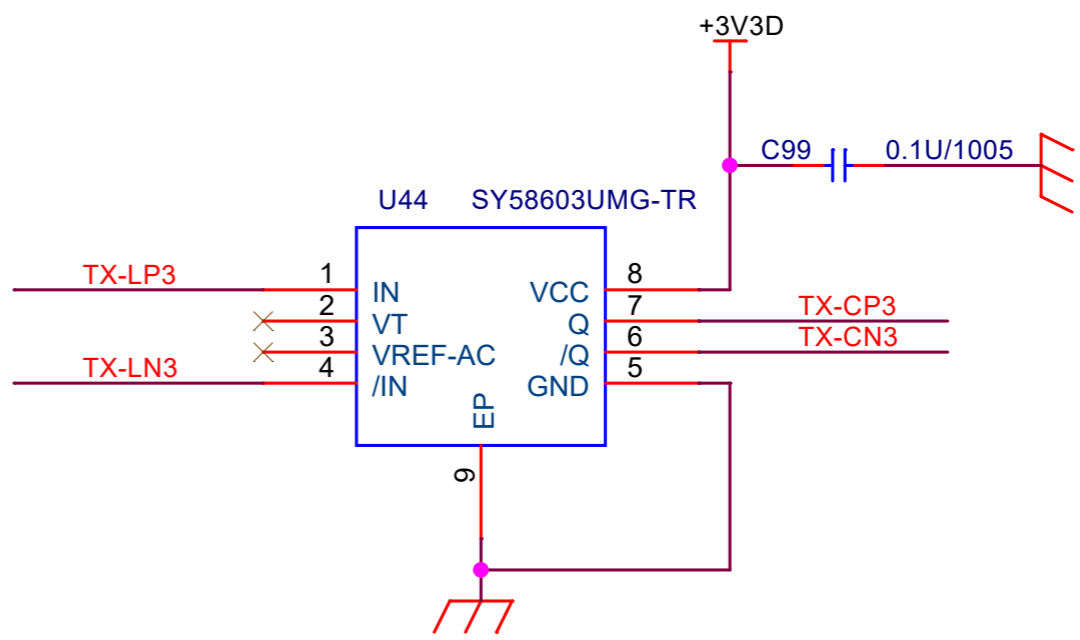
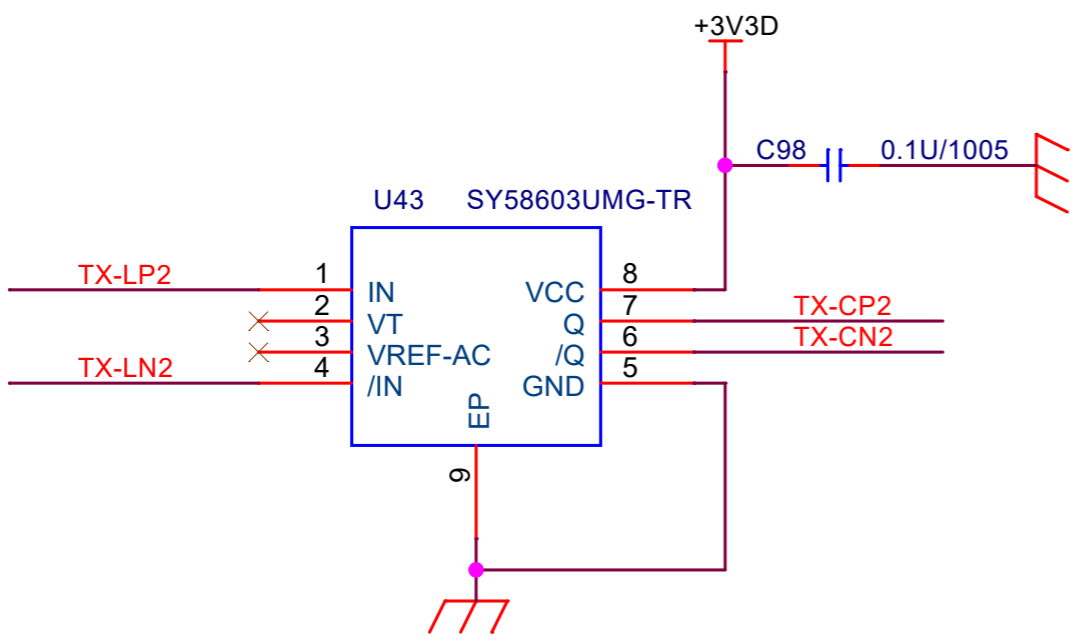
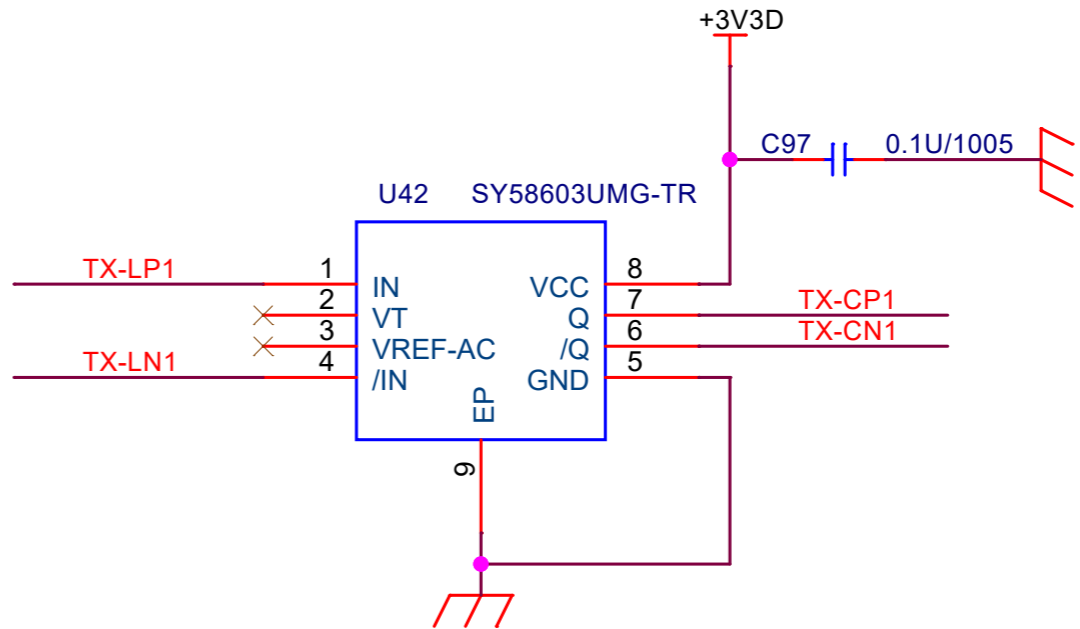
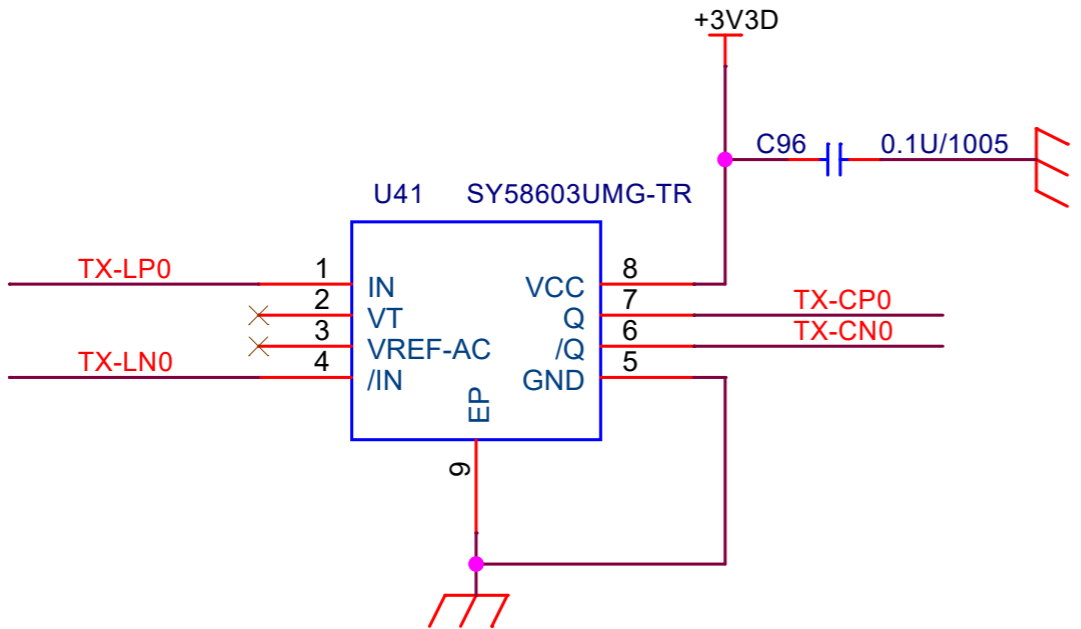
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TX-LP[3:0]  
TX-LN[3:0]



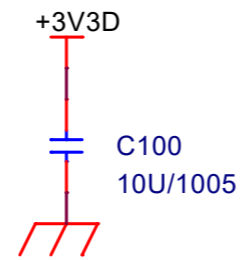
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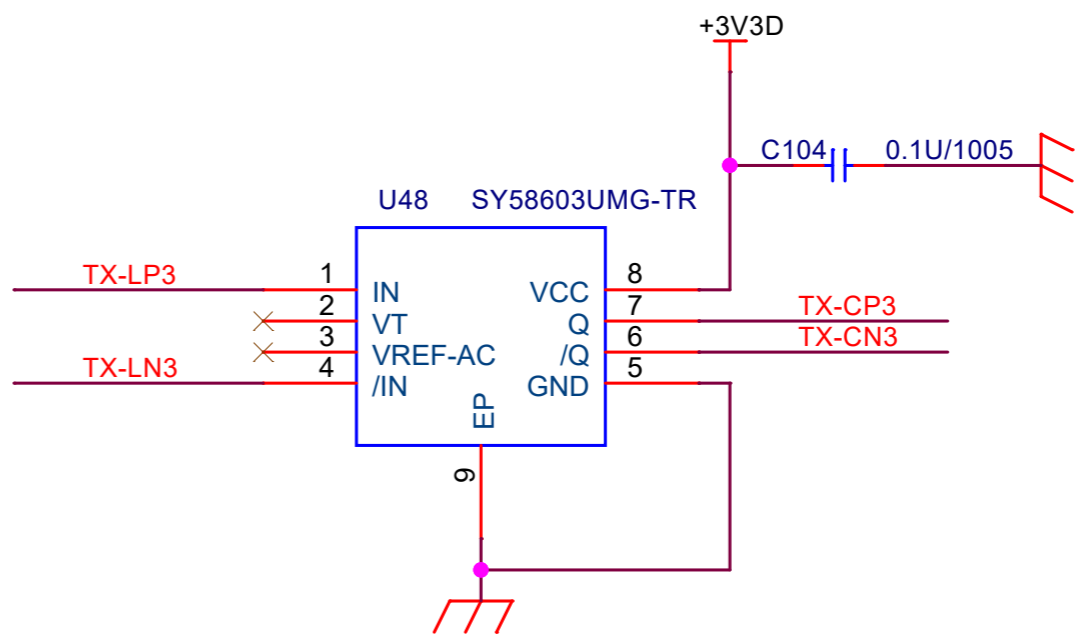
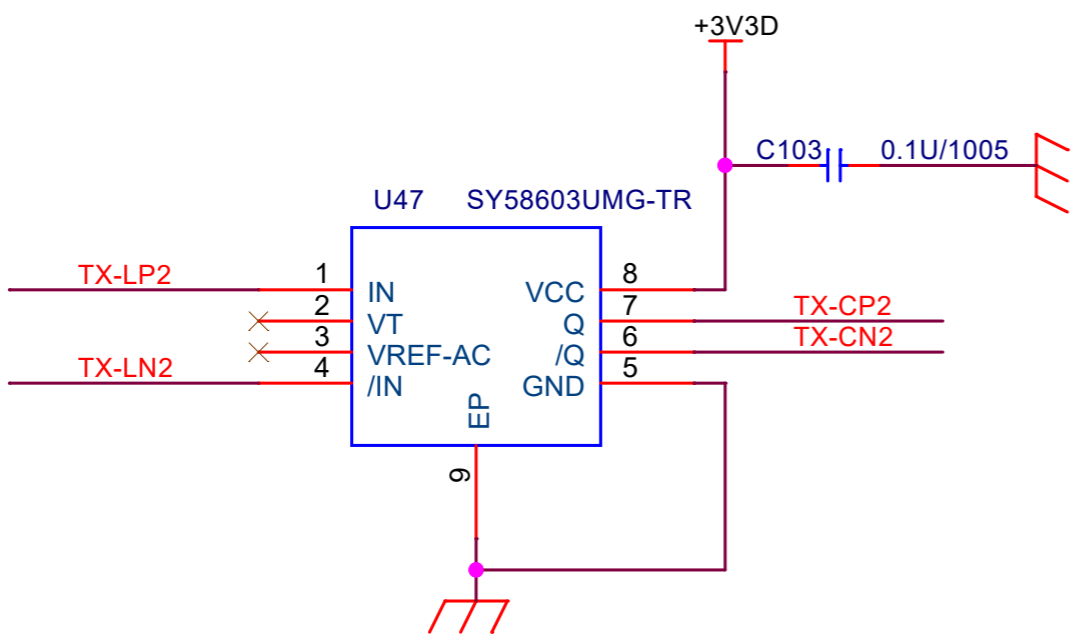
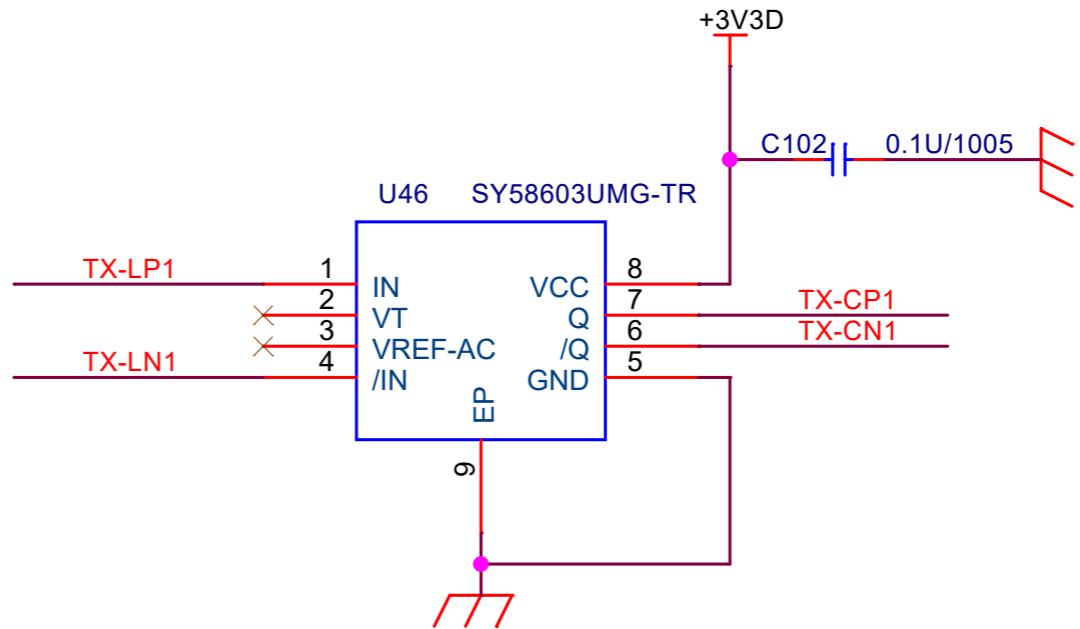
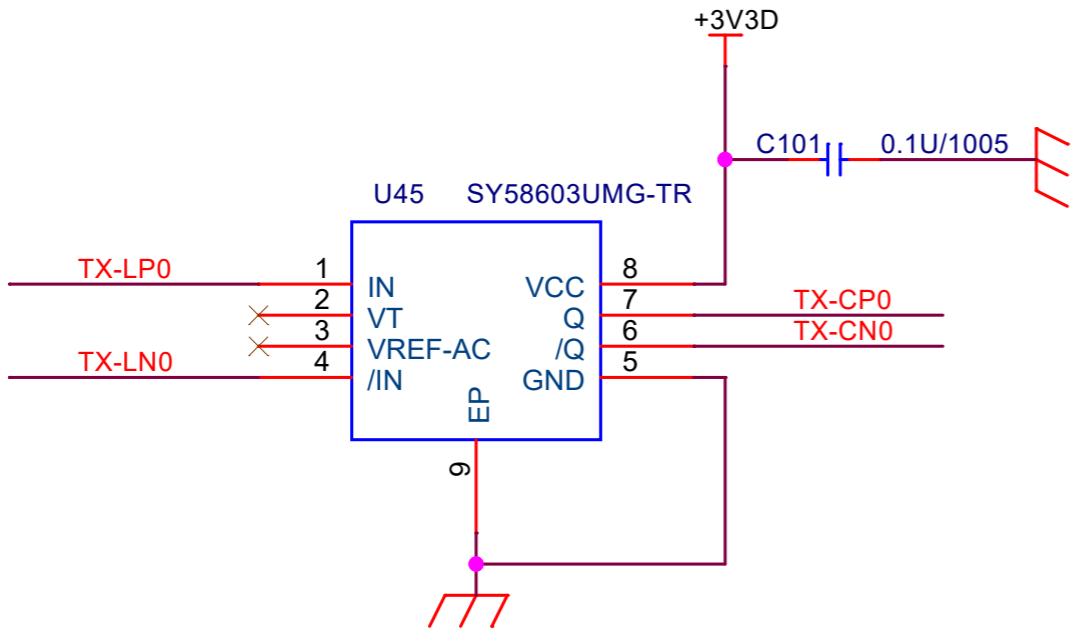
# LVDS-to-CML

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TX-LP[3:0]  
TX-LN[3:0]

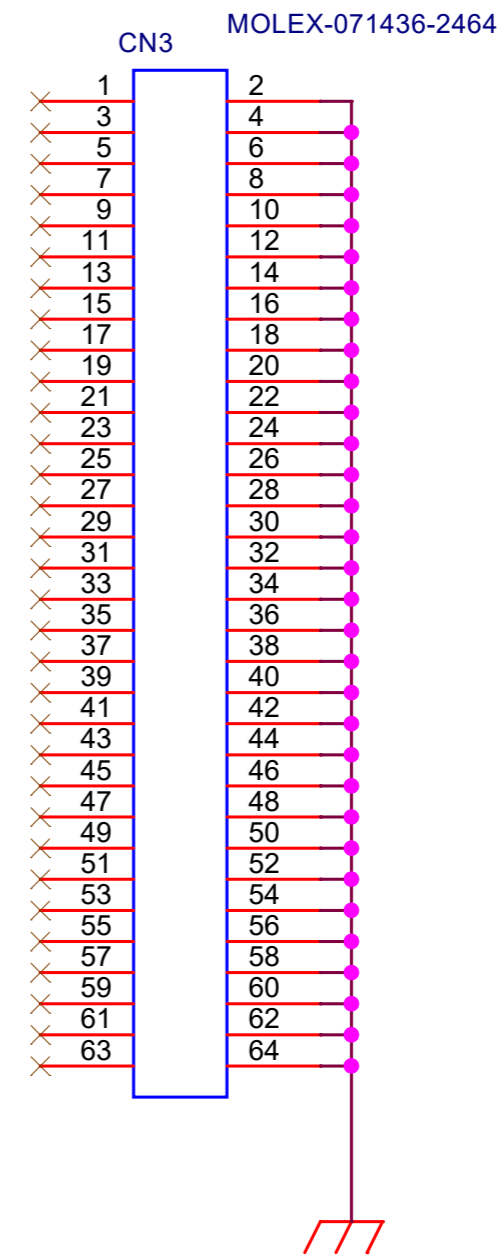
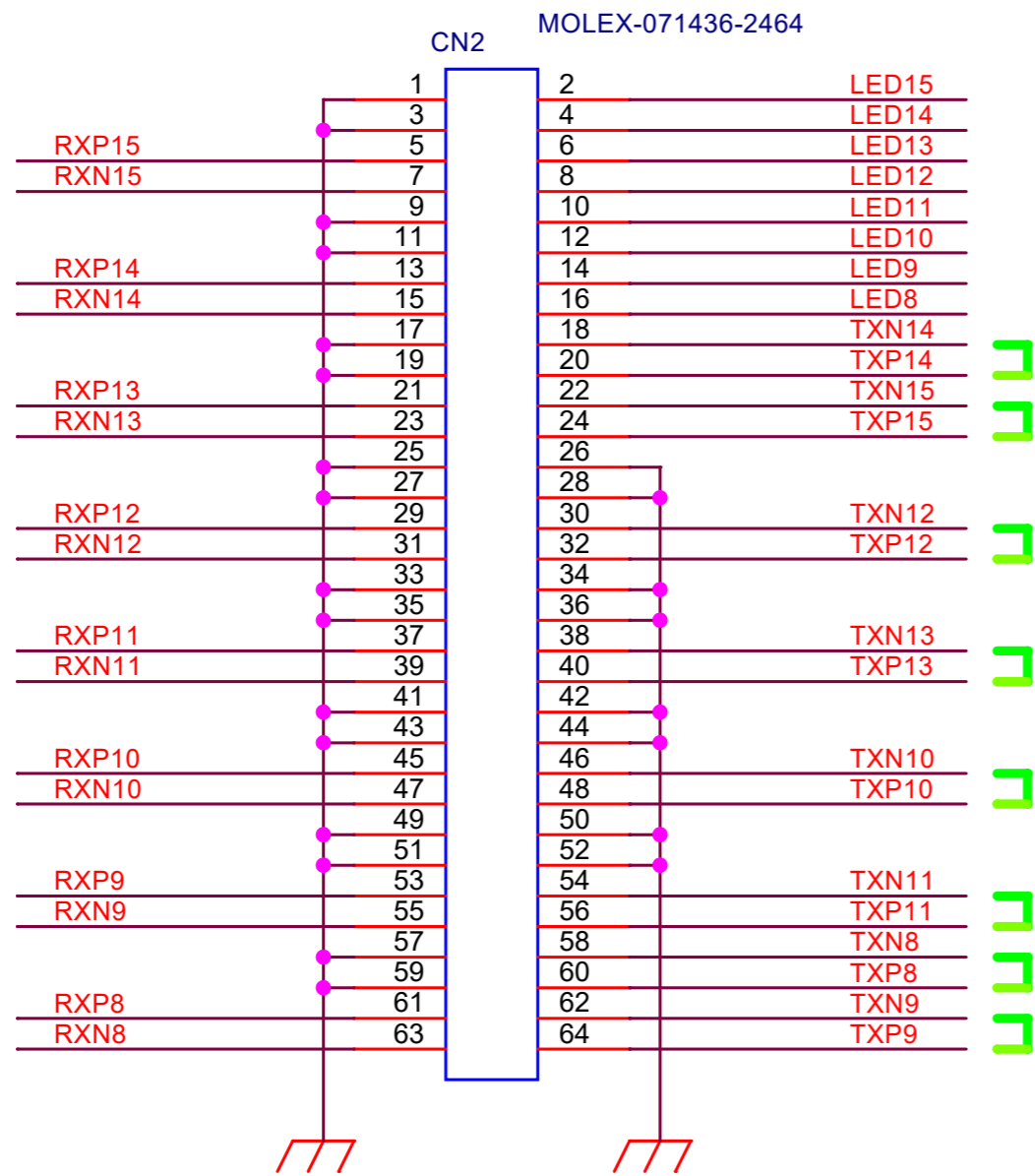
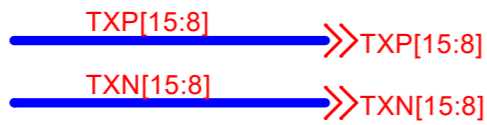


TX-CP[3:0]  
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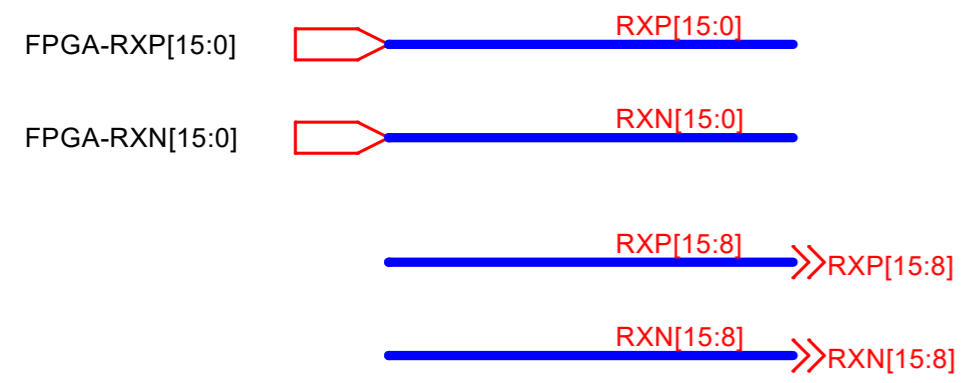
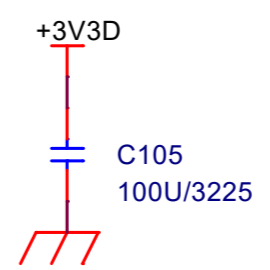
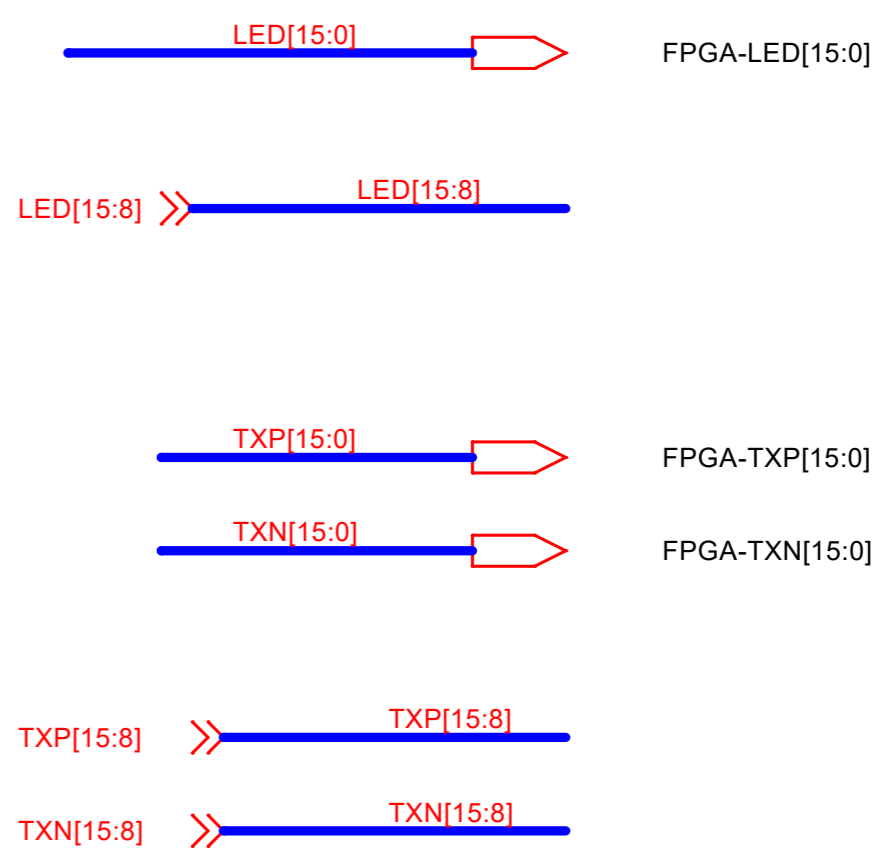
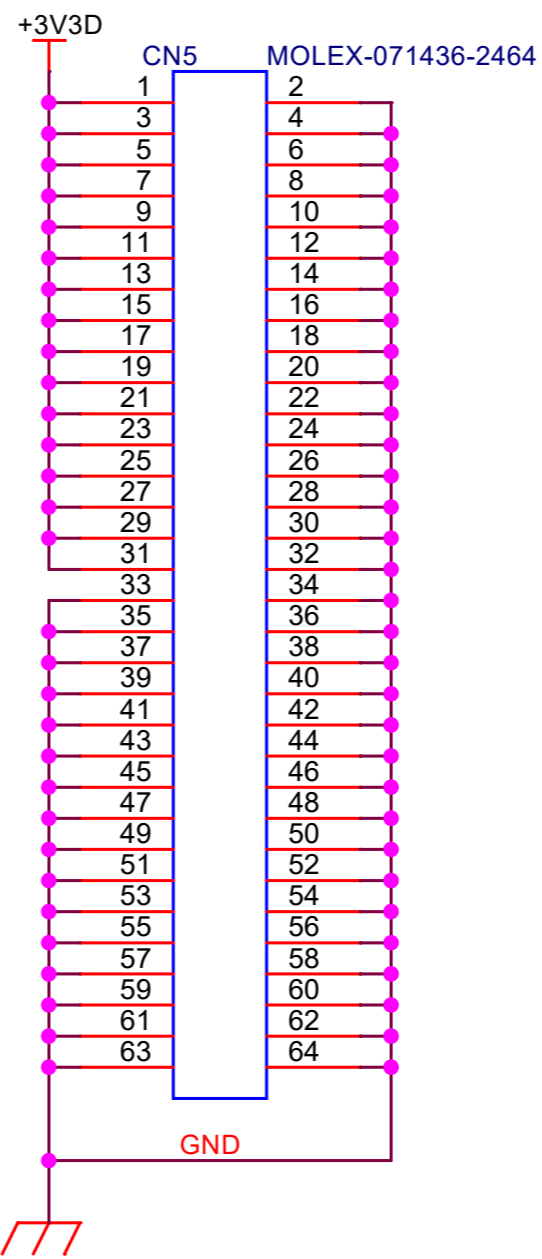
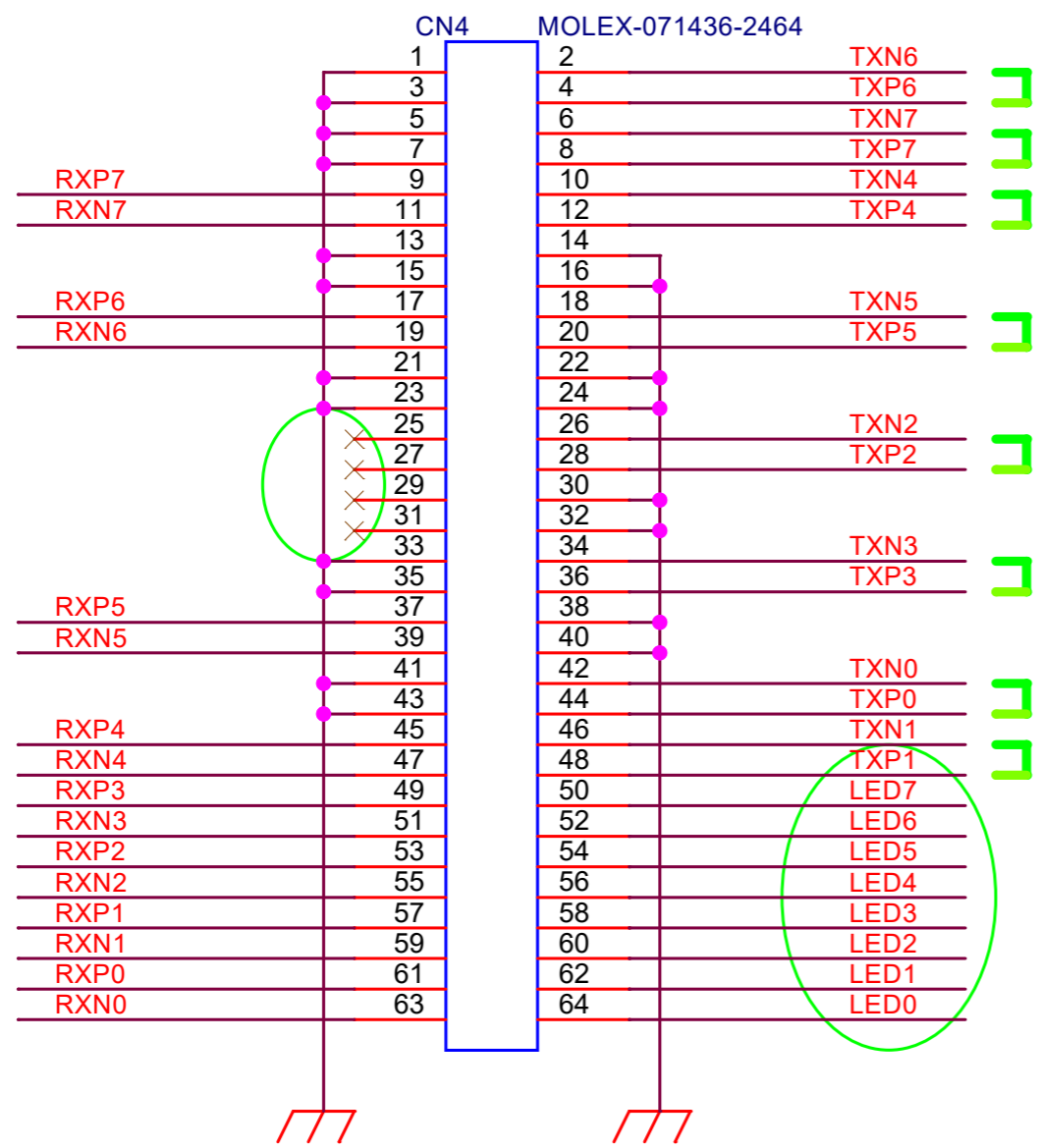


# LVDS-to-CML

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